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## Stability issues of finite-precision controller structures for sampled-data systems

J. WU<sup>+</sup>, R. H. ISTEPANIAN<sup>‡</sup> and S. CHEN<sup>§</sup>

The paper investigates the sensitivity of closed-loop stability with respect to (w.r.t.) finite word length (FWL) effects in the implementation of the digital controller coefficients. Both the shift and delta operators are considered for controller parameterization. Two tractable lower-bound measures of closed-loop stability are studied, and the optimal realization of general FWL controller structures is formulated as a constrained non-linear optimization problem. The emphasis of the paper, however, is on the derivation of a new algorithmic approach for the optimal realization of FWL PID controller structures. It is shown that, for PID structures, the optimization can be decoupled into two unconstrained problems with a maximum of four independent variables. An optimization strategy is developed to provide an efficient computational method for searching the optimal FWL PID controller realization with maximum stability bound and minimum bit-length requirement. Simulation results involving an IFAC benchmark PID controller system are presented to illustrate the effectiveness of the proposed strategy.

## 1. Introduction

The recent advances in fixed-point implementation of digital controllers, such as the design of dedicated fixed-point digital signal processor and digital control processor architectures, have made FWL implementation an important issue in modern control engineering. Improved control performance and increased levels of integration are particularly important in many application areas, such as consumer electronic products, automotive and electromechanical control systems. This is because controller hardware implementation with fixed-point arithmetic offer the advantages of speed, memory space, cost and simplicity over floating-point arithmetic (Masten and Panahi 1997). However, a designed stable closed-loop system may become unstable when the infinite-precision controller is implemented using a fixed-point processor due to FWL effects. The 'robustness' of closed-loop stability w.r.t. controller parameter perturbations therefore is a critical issue in fixed-point implementations and relevant control engineering applications.

In recent years, many results have been reported in the literature dealing with the issues of FWL controller implementation. The degradation effects of FWL on the digital controller designed using an LQG cost function has been investigated (Moroney *et al.* 1980). The effects of FWL implementated digital controller on the stability and performance of sampled-data systems has been analysed (Fialho and Georgiou 1994). A stability measure quantifying the FWL effects has been developed (Moroney *et al.* 1980, Fialho and Georgiou 1994). However, computing explicitly this measure is still an unsolved open problem. To overcome this computational difficulty, two tractable lower bounds of this stability measure have been derived (Li 1998, Istepanian *et al.* 1998 a). The criteria derived provide lower bounds proportional to the closed-loop pole sensitivity measures w.r.t. controller parameter perturbations. It can be shown that the lower bound of Istepanian *et al.* (1998) is a better stability measure than that of Li (1998).

Recent investigations on finite-precision controller realizations have mainly been based on these two lower-bounds of the stability measure and some similar criteria (Madievski et al. 1995, Istepanian et al. 1996, Li and Gevers 1996, Istepanian et al. 1998 b). The present study continues this theme with an emphasis on developing a new optimization method for the optimal realization of finite-precision controller structures. The problem is formulated as a constrained non-linear optimization problem. In particular, for PID sttuctures, the constrained optimization can be decoupled into two unconstrained optimization problems, which permits the development of an effective computational method for obtaining the optimal FWL PID realization with the maximum closed-loop stability measure. Notice that PID controllers have been the most popular controllers in process and industrial control applications for over 50 years and continue to maintain their popularity despite opportunities to apply more advanced control methodologies. This is because of their simplicity, versatility, robustness and successful commercial performance (Aström and Wittenmark 1989). Most of the studies in this area still focus on tuning methods, and very few studies have been reported to date on the FWL

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implementation issues of discrete PID structures using fixed-point arithmetic (Istepanian 1997).

In all the above-mentioned works addressing the closed-loop stability issues of FWL controller structures, the controllers were described and realized with the usual shift operator. It is known that discrete-time systems can also be described and realized with the delta operator (Middleton and Goodwin 1990). Two major advantages are claimed for the use of  $\delta$  operator parameterization: a theoretically unified formulation of continuous-time and discrete-time systems; and better numerical implementation properties (Gevers and Li 1993). As with the majority of earlier works, the results presented in this paper, when it was first submitted, were based on the shift operator parameterization. We have since extended the approach to the delta operator parameterization (Wu et al. 1999 a,b). These new results are included in this revised paper. Our simulation study confirms that the  $\delta$  operator parameterization generally results in better closed-loop stability robustness in FWL implementations, compared with the usual shift operator parameterization.

The paper is organized as follows. In § 2, a closedloop stability measure is presented for sampled-data systems with the shift operator parameterization and FWL implemented controllers. Two tractable lower bounds of this stability measure are considered. The optimal controller realization which maximizes the closed-loop stability measure can be obtained by solving a constrained optimization problem, and this is presented in § 3. Section 4 specifically studies the optimal realization of digital PID controllers subject to FWL constraints. Section 5 extends these results to include the delta operator parameterization. A practical bit length consideration is also discussed. In § 6, the effectiveness of the proposed optimization strategy for PID structures is illustrated by the numerical example of an IFAC benchmark PID control problem (Whidborne et al. 1995). Both the shift-operator and delta-operator controllers were tested in the simulation study. Discussions and some concluding remarks are given in § 7.

## 2. Stability robustness measures of z operator based controllers with FWL consideration

Consider the sampled-data system depicted in figure 1, where P(s) is the continuous-time finite-dimensional linear time-invariant plant, C(z) is the discrete-time finite-dimensional linear shift-invariant controller with z indicating the usual shift operator,  $S_h$  is the sampler with sampling period h, and  $H_h$  is the hold device. The outputs of the sampler and hold device are given by



Figure 1. Sampled-data system with digital controller realization.

$$y(z) = S_h y(s): \quad y(k) = y(t)|_{t=kh}$$
  

$$e(s) = H_h e(z): \quad e(t) = e(k) \text{ for } kh < t \le (k+1)h$$
(1)

respectively. Assume that P(s) is strictly proper. Let  $(A_p, B_p, C_p, 0)$  be a state-space realization of P(s), that is

$$\boldsymbol{P}(\boldsymbol{s}) = \boldsymbol{C}_p(\boldsymbol{s}\boldsymbol{I} - \boldsymbol{A}_p)^{-1}\boldsymbol{B}_p \tag{2}$$

where  $A_p \in \mathbb{R}^{m \times m}$ ,  $B_p \in \mathbb{R}^{m \times l}$  and  $C_p \in \mathbb{R}^{q \times m}$ . Let  $(A_c, B_c, C_c, D_c)$  be a state-space realization of C(z), that is

$$C(z) = C_c(zI - A_c)^{-1}B_c + D_c$$
 (3)

where  $A_c \in \pi^{n \times n}$ ,  $B_c \in \pi^{n \times q}$ ,  $C_c \in \pi^{l \times n}$  and  $D_c \in \pi^{l \times q}$ . The state-space realization for a given input–output transfer function is not unique. For example, if  $(A_c, B_c, C_c, D_c)$  is a realization of C(z), so is  $(\tau^{-1}A_c\tau, \tau^{-1}B_c, C_c\tau, D_c)$  for any similarity transformation  $\tau \in \pi^{n \times n}$ . Considering the behaviour of the sampled-data system at its sampling instants, we obtain a discrete-time feedback system

$$y(z) = S_h P(s) H_h e(z)^{\frac{1}{2}}$$

$$e(z) = C(z) y(z)$$
(4)

The plant  $P(z) = S_h P(s) H_h$  is the discretization of P(s), whose state-space realization is  $(A_z, B_z, C_z, 0)$  with

$$A_{z} = e^{A_{p}h} \in \mathbb{R}^{m \times m}$$

$$B_{z} = \int_{0}^{h} e^{A_{p}\tau} B_{p} d\tau \in \mathbb{R}^{m \times l}$$

$$C_{z} = C_{p} \in \mathbb{R}^{q \times m}$$
(5)

It can easily be seen that the corresponding state-space description  $(\overline{A}, \overline{B}, \overline{C}, \overline{D})$  of the discrete-time closed-loop system (4) without considering FWL effects is given by

$$\overline{A} = \begin{bmatrix}
A_{z} + B_{z}D_{c}C_{z} & B_{z}C_{c}
\end{bmatrix}$$

$$= \begin{bmatrix}
A_{z} & 0
\end{bmatrix} = \begin{bmatrix}
B_{z} & 0
\end{bmatrix} \begin{bmatrix}
B_{z} & 0
\end{bmatrix} \begin{bmatrix}
D_{c} & C_{c}
\end{bmatrix} \begin{bmatrix}
C_{z} & 0
\end{bmatrix}$$

$$= \begin{bmatrix}
A_{z} & 0
\end{bmatrix} + \begin{bmatrix}
B_{z} & 0
\end{bmatrix} \begin{bmatrix}
D_{c} & C_{c}
\end{bmatrix} \begin{bmatrix}
C_{z} & 0
\end{bmatrix}$$

$$= M_{0} + M_{1}XM_{2} = \overline{A}(X) \qquad (6)$$

$$\overline{B} = \begin{bmatrix}
B_{z} \\
0
\end{bmatrix}, \quad \overline{C} = [C_{z} & 0], \quad \overline{D} = 0 \qquad (7)$$

where  $M_0 \in \pi^{(m+n) \times (m+n)}$ ,  $M_1 \in \pi^{(m+n) \times (l+n)}$  and  $M_2 \in \pi^{(q+n) \times (m+n)}$  are some fixed matrices that depend on P(s) and h,  $I_n$  denotes the  $n \times n$  identity matrix, and

$$X = \begin{bmatrix} D_{c} & C_{c}^{\perp} \\ B_{c} & A_{c} \end{bmatrix}$$

$$= \begin{bmatrix} P_{1} & P_{2} & \cdots & P_{q+n} \\ P_{q+n+1} & P_{q+n+2} & \cdots & P_{2(q+n)} \\ \vdots & \vdots & \ddots & \vdots \\ P_{(l+n-1)(q+n)+1} & P_{(l+n-1)(q+n)+2} & \cdots & P_{(l+n)(q+n)} \end{bmatrix}$$
(8)

will be referred to as the controller matrix.

Suppose that C(z) has been chosen to make the sampled-data system stable and the realization of C(z) is X. Since the sampled-data system is stable if and only if the system (4) is stable (Chen and Francis 1991), it follows that the eigenvalues of  $\overline{A}(X)$ , denoted by  $(\lambda_i, 1 \le i \le m + n)$ , satisfy

$$|\lambda_i| < 1, \qquad \forall i \in \{1, \dots, m+n\}$$
(9)

When the realization  $(A_c, B_c, C_c, D_c)$  of C(z) is implemented with a fixed-point processor, the controller matrix X is perturbed into  $X + \Delta X$  due to the FWL effects, where

$$\Delta X = \begin{bmatrix} \Delta p_{1} & \Delta p_{2} & \dots & \Delta p_{q+n} \\ \Delta p_{q+n+1} & \Delta p_{q+n+2} & \dots & \Delta p_{2(q+n)} \\ \vdots & \vdots & \ddots & \vdots \\ \Delta p_{(l+n-1)(q+n)+1} & \Delta p_{(l+n-1)(q+n)+2} & \dots & \Delta p_{N} \end{bmatrix}$$
(10)

and N = (l + n)(q + n). Each element of  $\Delta X$  is bounded, that is

$$\mu(\Delta X) \triangleq \max_{i \in \{1, \dots, N\}} |\Delta p_i| \le \frac{\epsilon}{2} \tag{11}$$

For a fixed-point processor of  $B_s$  bits

$$\epsilon = 2^{-(B_s - B_X)} \tag{12}$$

where  $2^{B_X}$  is a 'normalization' factor such that the absolute value of each element of  $2^{-B_X}X$  is not larger than 1.

With the perturbation  $\Delta X$ ,  $\lambda_i$  is moved to  $\tilde{\lambda}_i$ . The closed-loop system is unstable if and only if there exists  $i \in \{1, \dots, m+n\}$  such that  $|\tilde{\lambda}_i| \ge 1$ .

To see when the round-off error will cause the closed-loop system to be unstable, define

$$\mu_0(X) \triangleq \inf \{ \mu(\Delta X) : \overline{A}(X) + M_1 \Delta X M_2 \text{ is unstable} \}$$
(13)

It quantifies the stability robustness of the realization X to the FWL effects. However, computing explicitly the value of  $\mu_0(X)$  is still an unsolved open problem. How 'robust' a controller realization is to the FWL effects can also be viewed from a different angle. Let  $B_s^{\min}$  be the smallest word length that can guarantee the closed-loop stability. It would be highly desirable to know  $B_s^{\min}$  for a given controller realization. However, except in simulation, it is impractical to test the closed-loop system by reducing  $B_s$  until it becomes unstable.

To overcome the difficulty in the computation of  $\mu_{o}(X)$ , Istepanian *et al.* (1998 a) introduced a lower bound of  $\mu_{0}(X)$  as

$$\mu_{1}(X) \triangleq \min_{i \in \{1, \dots, m+m\}} \frac{1 - |\lambda_{i}|}{\sum^{N} \left| \partial \lambda_{i} / \partial p_{j} \right|_{X} \right|}$$
(14)

We have the following theorem.

**Theorem 1:**  $\overline{A}(X) + M_1 \Delta X M_2$  is stable if  $\mu(\Delta X) < \mu_1(X)$ .

**Proof:** When  $\Delta X$  is small, using a first-order approximation we have (Li and Gevers 1996, Istepanian *et al.* 1998 a)

$$\Delta_{\lambda_{i}} = \tilde{\lambda}_{i} - \lambda_{i} \approx \frac{\sum_{j=1}^{N} \frac{\partial \lambda_{i}}{\partial p_{j}}}{\sum_{j=1}^{N} \frac{\partial \lambda_{j}}{\partial p_{j}}} \Delta_{p_{j}}, \qquad 1 \leq i \leq m + n \quad (15)$$

where  $\tilde{\lambda}_i$  are the eigenvalues of  $\overline{A}(X + \Delta X)$ . It follows that

$$|\Delta_{\lambda_{i}|} \leq \sum_{j=1}^{\Sigma^{N}} \left| \frac{\partial \lambda_{i}}{\partial p_{j}} \right|_{X} \left| |\Delta_{p_{j}|} \leq \mu(\Delta_{X}) \sum_{j=1}^{\Sigma^{N}} \left| \frac{\partial \lambda_{i}}{\partial p_{j}} \right|_{X} \right| \quad (16)$$

Thus for  $1 \leq i \leq m + n$ , if

$$\mu(\Delta X) < \frac{1 - |\lambda_i|}{\sum_{j=1}^{N} |\partial \lambda_i / \partial p_j|_X|}$$
(17)

we have

1

J. Wu et al.

$$\begin{split} |\tilde{\lambda_{i}}| &\leq |\lambda_{i}| + |\Delta_{\lambda_{i}}| \leq |\lambda_{i}| + |\mu(\Delta_{X})|^{\sum^{N}} \left| \frac{\partial \lambda_{i}}{\partial p_{j}} \right|_{X} \\ &< |\lambda_{i}| + \frac{1 - |\lambda_{i}|}{\sum^{N}} \frac{\sum^{N}}{\left| \partial \lambda_{i} / \partial p_{j} \right|_{X}} \left| \frac{\partial \lambda_{i}}{\partial p_{j}} \right|_{X} \right| = 1 \end{split}$$
(18)

which means that  $\overline{A}(X + \Delta X)$  is stable.

The following lemma shows that  $\mu_1(X)$  can be computed easily. The proofs of this lemma can be found in Istepanian *et al.* (1998 a).

**Lemma 1:** Let  $\overline{A}(X)$  be diagonalizable and have  $\{\lambda_i, i = 1, \ldots, m + n\}$  as its eigenvalues, and  $\mathbf{x}_i$  be a right eigenvector of  $\overline{A}(X)$  corresponding to the eigenvalue  $\lambda_i$ . Denote  $M_x = [\mathbf{x}_1 \cdots \mathbf{x}_{m+n}]$  and  $M_y = [\mathbf{y}_1 \cdots \mathbf{y}_{m+n}] = M_x^{-H}$ , where  $\mathbf{y}_i$  is called the reciprocal left eigenvector corresponding to  $\lambda_i$ , and  $^H$  denotes the transpose and conjugate operation. Then  $\forall i \in \{1, \ldots, m + n\}$ 

$$\frac{\partial \lambda_{i}}{\partial p_{1}} \qquad \frac{\partial \lambda_{i}}{\partial p_{2}} \qquad \cdots \qquad \frac{\partial \lambda_{i}}{\partial p_{q+n}}$$

$$\frac{\partial \lambda_{i}}{\partial p_{q+n+1}} \qquad \frac{\partial \lambda_{i}}{\partial p_{q+n+2}} \qquad \cdots \qquad \frac{\partial \lambda_{i}}{\partial p_{2(q+n)}}$$

$$\vdots \qquad \vdots \qquad \cdots \qquad \vdots$$

$$\frac{\partial \lambda_{i}}{\partial p_{(l+n-1)(q+m+1)}} \qquad \frac{\partial \lambda_{i}}{\partial p_{(l+n-1)(q+m+2)}} \qquad \cdots \qquad \frac{\partial \lambda_{i}}{\partial p_{N}}$$

$$= M_{1}^{T} \mathbf{y}_{i}^{*} \mathbf{x}_{i}^{T} M_{2}^{T} \qquad (19)$$

where  $^{T}$  denotes the transpose operation, and \* the conjugate operation.

When a designed infinite-precision stable controller X is implemented with a fixed-point processor, the norm of the controller perturbation  $\mu(\Delta X)$  and the lowerbound stability measure  $\mu_1(X)$  can be evaluated. If  $\mu_1(X) > \mu(\Delta X)$ , the closed-loop stability is maintained. Furthermore, when X is implemented with a fixed-point processor of  $B_s$  bits, from (11) and Theorem 1, it is easily seen that the closed-loop system is stable if

$$\mu_1(X) > \frac{2^{-(B_{s^-}, B_X)}}{2} \tag{20}$$

Define  $\hat{B}_{s1}^{\min}$  as the smallest integer that is not less than -  $\log_2 \mu_1(X) - 1 + B_X$ . We can use  $\hat{B}_{s1}^{\min}$  as a super estimate of  $B_s^{\min}$ . Thus,  $\mu_1(X)$  provides a tractable closed-loop stability robustness measure of X with FWL considerations.

Another tractable stability robustness measure with FWL considerations was discussed by Li and Gevers (1996) and Li (1998). This measure is defined as

$$\mu_{2}(X) \triangleq \min_{i \in \{1,...,m+n\}} \frac{1 - |\lambda_{i}|}{\left| \sum_{j=1}^{N} \left| \partial \lambda_{i} / \partial p_{j} \right|_{X} \right|^{2}}$$
(21)

It is also a lower bound of  $\mu_0(X)$ . Similarly, an estimate  $\hat{B}_{s2}^{\min}$  of  $B_s^{\min}$  can be computed based on  $\mu_2(X)$ . Since

$$\sum_{j=1}^{N} \left| \frac{\partial \lambda_{i}}{\partial p_{j}} \right|_{X} \right|^{2} \leq N \sum_{j=1}^{N} \left| \frac{\partial \lambda_{i}}{\partial p_{j}} \right|_{X}^{2}$$
(22)

we have  $\mu_2(X) \leq \mu_1(X) \leq \mu_0(X)$ . It is clear that  $\mu_1(X)$ , which is closer to  $\mu_0(X)$ , is a better stability robustness measure and can provice a better estimate of  $B_2^{\min}$ .

## 3. Optimal realization of z operator based controller structures with FWL consideration

From the previous section, we know that there are different realizations X for a given C(z), and the stability measure  $\mu_1(X)$  is a function of the realization. It is of practical importance to find a realization such that  $\mu_1(X)$  is maximized. Such a realization is optimal in the sense that it has maximum closed-loop stability robustness to FWL effects. The digital controller implemented with an optimal realization means that the stability of the closed-loop system is guaranteed with a minimum hardware requirement in terms of word length. Given an initial realization  $X_0$  of C(z)

$$X_0 = \frac{\begin{bmatrix} D_c^0 & C_c^0 \end{bmatrix}}{B_c^0 & A_c^0} \tag{23}$$

any realization of C(z) can be expressed as:

$$X_{\tau} \triangleq \begin{bmatrix} I_l & 0\\ 0 & \tau^{-1} \end{bmatrix} X_0 \begin{bmatrix} I_q & 0\\ 0 & \tau \end{bmatrix}$$
(24)

where  $\tau \in \pi^{n \times n}$  and det $(\tau) \neq 0$ . From (6), the closed-loop transition matrix is

$$\overline{A}(X_{\tau}) = \begin{bmatrix} A_{z} & 0\\ 0 & 0 \end{bmatrix} + \begin{bmatrix} B_{z} & 0\\ 0 & I_{n} \end{bmatrix} \begin{bmatrix} I_{l} & 0\\ 0 & \tau^{-1} \end{bmatrix} \\
\times X_{0} \begin{bmatrix} I_{q} & 0\\ 0 & \tau \end{bmatrix} \begin{bmatrix} C_{z} & 0\\ 0 & I_{n} \end{bmatrix} \\
= \begin{bmatrix} I_{m} & 0\\ 0 & \tau^{-1} \end{bmatrix} \begin{bmatrix} A_{z} & 0\\ 0 & 0 \end{bmatrix} \begin{bmatrix} I_{m} & 0\\ 0 & \tau \end{bmatrix} \\
+ \begin{bmatrix} I_{m} & 0\\ 0 & \tau^{-1} \end{bmatrix} \begin{bmatrix} B_{z} & 0\\ 0 & I_{n} \end{bmatrix} \\
\times X_{0} \begin{bmatrix} C_{z} & 0\\ 0 & I_{n} \end{bmatrix} \begin{bmatrix} I_{m} & 0\\ 0 & \tau \end{bmatrix} \\
= \begin{bmatrix} I_{m} & 0\\ 0 & \tau^{-1} \end{bmatrix} \overline{A}(X_{0}) \begin{bmatrix} I_{m} & 0\\ 0 & \tau \end{bmatrix} \quad (25)$$

1334

Obviously,  $\overline{A}(X_{\tau})$  has the same eigenvalues as  $\overline{A}(X_0)$ . Let  $\lambda_i^0$  be the *i*th eigenvalue of  $\overline{A}(X_0)$ , and  $\mathbf{x}_i^0$  and  $\mathbf{y}_i^0$  be the corresponding right and reciprocal left eigenvectors, respectively. From (25), the *i*th right and reciprocal left eigenvectors of  $\overline{A}(X_{\tau})$  are

$$\begin{bmatrix} I_m & 0\\ 0 & \tau^{-1} \end{bmatrix} \mathbf{x}_i^0 \in c^{m+n} \text{ and } \begin{bmatrix} I_m & 0\\ 0 & \tau^{\mathrm{T}} \end{bmatrix} \mathbf{y}_i^0 \in c^{m+n}$$
(26)

respectively. Applying Lemma 1, we have

$$\frac{\partial \lambda_{i}}{\partial X}\Big|_{X=X_{\tau}} = \frac{\begin{bmatrix} B_{z}^{T} & 0 \end{bmatrix} \begin{bmatrix} I_{m} & 0 \end{bmatrix}}{0 & I_{n} & 0 & \tau^{T} \end{bmatrix} \left(y_{i}^{0}\right)^{*}(x_{i}^{0})^{T} \\
\times \begin{bmatrix} I_{m} & 0 \end{bmatrix} \begin{bmatrix} C_{z}^{T} & 0 \end{bmatrix} \\
0 & \tau^{-T} & 0 & I_{n} \end{bmatrix} \\
= \begin{bmatrix} I_{l} & 0 \end{bmatrix} \begin{bmatrix} B_{z}^{T} & 0 \end{bmatrix} \\
0 & \tau^{T} & 0 & I_{n} \end{bmatrix} \left(y_{i}^{0}\right)^{*}(x_{i}^{0})^{T} \\
\times \begin{bmatrix} C_{z}^{T} & 0 \end{bmatrix} \begin{bmatrix} I_{q} & 0 \end{bmatrix} \\
0 & I_{n} & 0 & \tau^{-T} \end{bmatrix} \\
= \begin{bmatrix} I_{l} & 0 \end{bmatrix} \left(y_{i}^{0}\right)^{*}(x_{i}^{0})^{T} \\
= \begin{bmatrix} I_{i} & 0 \end{bmatrix} \\
= \begin{bmatrix} I_{i}$$

From (14), (19) and (27), the optimal realization problem of FWL controllers can be defined as the following maximization problem

$$\varphi \triangleq \max_{X_{\tau}} \mu_{1}(X_{\tau}) = \max_{X_{\tau}} \min_{1 \le i \le m_{\tau}} \frac{1 - |\lambda_{i}^{0}|}{\sum_{j=1}^{N} \left| \partial \lambda_{i} / \partial p_{j} \right|_{X=X_{\tau}}} \right|$$

$$(28)$$

For the complex-valued matrix  $M \in c^{m \times n}$  with elements  $M_{i,j}$ , define a norm

$$\|M\|_{s} \triangleq \sum_{i=1}^{m} \sum_{j=1}^{n} |M_{i,j}|$$
(29)

The optimization problem (28) is equivalent to the minimization problem

$$v = \frac{1}{\varphi} \triangleq \min_{X_{\tau}} \max_{1 \le i \le m_{+}} \frac{\left\| (\partial \lambda_{i} / \partial X) \right\|_{X_{=} X_{\tau}} }{1 - \|\lambda_{i}^{0}\|}$$
$$= \min_{\substack{\tau \in \mathbb{R}^{n \times n} \\ \det(\tau) \neq 0}} \max_{1 \le i \le m_{+} n} \left\| \begin{bmatrix} I_{l} & 0 \end{bmatrix} \begin{bmatrix} I_{q} & 0 \end{bmatrix} \\ 0 & \tau^{\mathsf{T}} & \Phi_{i} \\ 0 & \tau^{\mathsf{T}} & 0 & \tau^{\mathsf{T}} \end{bmatrix} \right\|_{s}$$
(30)

where

$$\Phi_{i} = \frac{(\partial \lambda_{i} / \partial X)|_{X = X_{0}}}{1 - |\lambda_{i}^{0}|}, \qquad 1 \leq i \leq m + n \qquad (31)$$

are the eigenvalue sensitivity matrices.

Thus the optimal controller realization problem is posed as an optimization problem with the cost function

$$f(\tau) = \max_{1 \le i \le m+n} \left\| \begin{bmatrix} I_l & 0 \end{bmatrix} \begin{bmatrix} I_q & 0 \end{bmatrix} \\ 0 & \tau \end{bmatrix} \left\| \begin{bmatrix} I_q & 0 \end{bmatrix} \\ 0 & \tau \end{bmatrix} \right\|_{s}$$
(32)

and the constraint det  $(\tau) \neq 0$ . Evidently, the cost function (32) is non-smooth and non-convex, and optimization must be based on a direct search without the aid of cost function derivatives. The conventional optimization methods for this kind of problem, such as Rosenbrock and Simplex algorithms (Kowalik and Osborne 1968, Beveridge and Schechter 1970, Dixon 1972), in general can only find a local minimum. A more serious problem, however, is the need to satisfy  $det(\tau) \neq 0$  during the optimization process. It is well known that constrained optimization is more difficult to solve, compared with unconstrained optimization. In all the previous works (Gevers and Li 1993, Li and Gevers 1996, Istepanian et al. 1996, 1998 a,b), similar optimization problems were solved with some success by using a conventional direct search algorithm and ignoring the constraints during optimization. Nevertheless, the possible pitfall of violating the constraint by this kind of approach remains, which may result in an invalid solution.

# 4. Optimal realization of z operator based PID controller structures with FWL consideration

From the previous discussion, we know there is a need to develop some new efficient numerical algorithm for solving the optimal FWL controller realization problem. A main contribution of this paper is to show how this can be achieved for the optimal FWL PID controller realization problem. Assume that C(z) is a digital physically realizable non-interacting PID controller structure (Rad and Lo 1994). Let  $(A_c^0 \in \pi^{2\times 2}, B_c^0 \in \pi^{2\times 1}, C_c^0 \in \pi^{1\times 2}, D_c^0 \in \pi)$  be an initial realization of the PID controller C(z). From (30), the optimal FWL PID controller realization problem is defined as the optimization problem

$$v \triangleq \min_{\substack{\tau \in \mathbb{R}^{2\times 2} \\ \det(\tau) \neq 0}} \max_{1 \le i \le m+2} \left\| \begin{bmatrix} I_i & 0 \end{bmatrix} & \begin{bmatrix} I_q & 0 \end{bmatrix} \\ 0 & \tau^{\mathsf{T}} & \Phi_i \\ 0 & \tau^{\mathsf{T}} & \end{bmatrix}_{\mathcal{S}} (33)$$

As is difficult to handle the constraint det  $(\tau) \neq 0$ directly in numerical optimization, we show in the following theorem that the optimization problem (33) can be decoupled into the two 'simpler' unconstrained problems. First we define the two cost functions

$$f_{1}(x, y, w) = \max_{1 \le i \le m+2} \left\| \begin{bmatrix} w & 0 & 0 & | & | & 1/w & 0 & 0 \\ | & 0 & x & 0 & | & \Phi_{i_{1}} & 0 & 1/x & 0 \\ | & 0 & y & 1/x & 0 & -y & x \\ \end{bmatrix} \right\|_{s}$$
(34)

and

 $f_2$ 

$$(x, y, u, w) = \max_{1 \le i \le m+2} \left\| \begin{bmatrix} w & 0 & 0 \\ 0 & x & u \end{bmatrix} \\ 0 & (xy - 1)/u & y \end{bmatrix} \\ \times \Phi_{i} \begin{bmatrix} 1/w & 0 & 0 \\ 0 & y & -u \end{bmatrix} \\ 0 & (1 - xy)/u & x \end{bmatrix}_{s}^{s}$$
(35)

Theorem 2: Let

$$v_{1} = \min_{\substack{x \in (0, +\infty) \\ y \in (-\infty, +\infty) \\ w \in (0, +\infty)}} f_{1}(x, y, w)$$
(36)

and

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$$v_{2} = \min_{\substack{x \in (-\infty, +\infty) \\ y \in (-\infty, +\infty) \\ u \in (0, +\infty) \\ w \in (0, +\infty)}} f_{2}(x, y, u, w)$$
(37)

Then

$$v = \min\{v_1, v_2\}$$
 (38)

If  $v = v_1$  and  $(x_{opt1}, y_{opt1}, w_{opt1})$  is the optimal solution of problem (36), the optimal solution of problem (33) is given as

$$\tau_{opt} = \frac{1}{w_{opt1}} \begin{bmatrix} x_{opt1} & y_{opt1} \end{bmatrix}$$
(39)

If  $v = v_2$  and  $(x_{opt2}, y_{opt2}, u_{opt2}, w_{opt2})$  is the optimal solution of problem (37), the optimal solution of problem (33) is given as

$$\tau_{opt} = \frac{1}{w_{opt2}} \begin{bmatrix} x_{opt2} & (x_{opt2}y_{opt2} - 1)/u_{opt2} \end{bmatrix}$$
(40)

The proof of Theorem 2 is given in the Appendix. Note that  $f_1(x, y, w)$  and  $f_2(x, y, u, w)$  are still nonsmooth and non-convex functions, and it may be difficult for a conventional non-gradient-based algorithm (Kowalik and Osborne 1968, Beveridge and Schechter 1970, Dixon 1972) to obtain a global minimum solution. This difficulty, however, can be overcome by employing an effcient global optimization strategy, such as the genetic algorithm (GA) (Goldberg 1989, Davis 1991, Man et al. 1997) or the adaptive simulated annealing (ASA) (Ingber and Rosen 1992, Ingber 1996, Rosen 1997, Chen et al. 1998). In this study, we adopt the ASA for its simplicity and ease of programming. The detailed implementation of the ASA algorithm can be found in Ingber and Rosen (1992), Ingber (1996), Rosen (1997) and Chen et al. (1998). It is equally valid to adopt the GA in the optimization.

#### J. Wu et al.

#### 5. Extension to $\delta$ operator parameterization

The results presented in \$ 2–4 are derived based on the z operator parameterization. These have been extended to delta operator based controllers in a new study (Wu *et al.* 1999 a,b). The delta operator is defined as (Middleton and Goodwin 1990)

$$\delta = \frac{z - 1}{h} \tag{41}$$

Let a state-space realization of the  $\delta$ -based controller  $C(\delta)$  be  $(A_{\delta,c}, B_{\delta,c}, C_{\delta,c}, D_{\delta,c})$ . The subscript  $\delta$  distinguishes this model from the z-based controller realization  $(A_c, B_c, C_c, D_c)$ . A state-space model of the closed-loop system in the  $\delta$  domain is  $(\overline{A}_{\delta}, \overline{B}_{\delta}, \overline{C}_{\delta}, \overline{D}_{\delta})$  with the eigenvalues of  $\overline{A}_{\delta}$  being  $(\lambda_{\delta,i}, 1 \leq i \leq m + n)$ . Notice that, just as in the z-based case of (6),  $\overline{A}_{\delta} = \overline{A}_{\delta}(X_{\delta})$  is a function of the controller matrix

$$X_{\delta} = \begin{bmatrix} D_{\delta,c} & C_{\delta,c} \\ B_{\delta,c} & A_{\delta,c} \end{bmatrix}$$
(42)

The relationships between the *z* and  $\delta$  parameterizations are well known (Neuman 1993 a,b). For example, two state-space models of C(z) and  $C(\delta)$  are linked by

$$A_{c} = hA_{\delta,c} + I_{n}, \quad B_{c} = hB_{\delta,c}, \quad C_{c} = C_{\delta,c}, \quad D_{c} = D_{\delta,c}$$
(43)

and the two sets of the eigenvalues  $\{\lambda_i\}$  and  $\{\lambda_{\delta,i}\}$  satisfy

$$\lambda_i = 1 + h\lambda_{\delta,i}, \quad \forall i \tag{44}$$

From (44), we have the condition of closed-loop stability in the  $\delta$  domain.

**Lemma 2:** The discrete-time system  $(\overline{A}_{\delta}, \overline{B}_{\delta}, \overline{C}_{\delta}, \overline{D}_{\delta})$  is stable if and only if

$$\left|\lambda_{\delta,i} + \frac{1}{h}\right| < \frac{1}{h}, \quad \forall i$$
(45)

We can now summarize the main results of Wu *et al.* (1999 a,b). Similar to (14), a lower-bound stability measure for  $\delta$ -based FWL controllers is

$$\mu_{1}(X_{\delta}) \triangleq \min_{1 \leq i \leq m+} \frac{(1/h) - |\lambda_{\delta,i} + (1/h)|}{\sum_{j=1}^{N} \left| \partial \lambda_{\delta,i} / \partial p_{j} \right|_{X_{\delta}} \right|$$
(46)

where  $p_j$  are the elements of  $X_{\delta}$ . The optimal realization problem of  $\delta$ -based controller structures with FWL consideration is posed as a constrained optimization problem with the cost function  $f(\tau)$  as defined in (32) and subject to the constraint det $(\tau) \neq 0$ , but the eigenvalue sensitivity matrices are now given differently by

$$\Phi_{i} = \frac{(\partial \lambda_{\delta,i} / \partial X) |_{X = X_{\delta,0}}}{(1/h) - |\lambda_{\delta,i}^{0} + (1/h)|}$$
(47)

where  $X_{\delta,0}$  is the initial realization of the controller matrix and  $\lambda_{\delta,i}^0$  the eigenvalues of  $A_{\delta}(X_{\delta,0})$ . The computation of  $\partial \lambda_{\delta,i} / \partial X$  is the same as given in (19) but the matrices  $M_1$  and  $M_2$  are now formed differently from the state-space model of the  $\delta$ -based plant model  $P(\delta)$ . It can also be shown that the optimal realization problem of  $\delta$ -based FWL PID controller structures can be decoupled into two unconstrained optimization problems and a theorem similar to Theorem 2 can be proved (Wu *et al.* 1999 a).

It is worth pointing out a practical constraint on the FWL implementation of  $\delta$ -based controllers, which is often overlooked. The state-space equation of the  $\delta$ -based controller

$$\delta \mathbf{X}(k) = A_{\delta,c} \mathbf{X}(k) + B_{\delta,c} \mathbf{u}(k) \tag{48}$$

is realized using

$$x(k + 1) = x(k) + h(A_{\delta,c}x(k) + B_{\delta,c}u(k))$$
(49)

The sampling period *h* should be implemented exactly without any FWL errors in order to avoid further perturbations to the controller  $X_{\delta}$ . Otherwise, analysis based on  $X_{\delta}$  may not be valid. Notice that controllers based on the *z* operator do not have this problem.

More specifically, assume that *h* can be realized exactly by  $B_h$  bits with the integer part of *h* requiring  $B_{h,I}$  bits and the fractional part of *h* requiring  $B_{h,F}$  bits. Let  $\hat{B}_{s1}^{\min}$  be the smallest integer that is not less than  $-\log_2 \mu_1(X_{\delta}) - 1 + B_X$ . Here  $2^{B_X}$  is the normalization factor for  $X_{\delta}$ . In the *z*-based case, we can use  $\hat{B}_{s1}^{\min}$  as an estimated minimum bit length that can guarantee the closed-loop stability. In the  $\delta$ -based case, this needs modification to take into account the requirement of implementing *h* exactly. A modified estimate of the minimum bit length that can guarantee the closed-loop stability is

$$\hat{B}_{sh}^{\min} = \max \{ B_{h,I}, B_X \} + \max \{ B_{h,F}, \hat{B}_{s1}^{\min} - B_X \}$$
 (50)

For example, if  $h = 2^3$  and  $\hat{B}_{s1}^{\min} = 8$  with  $B_X = 1$ , the estimated minimum bit length is  $\hat{B}_{sh}^{\min} = 3 + (8 - 1) = 10$ . If  $h = 2^{-10}$  and  $\hat{B}_{s1}^{\min} = 4$  with  $B_X = 1$ ,  $\hat{B}_{sh}^{\min} = 1 + 10 = 11$ .

### 6. A numerical example

To show how the optimization approach presented earlier can be used efficiently for designing optimal FWL PID controller structures, we consider the following IFAC benchmark PID control system (Whidborne *et al.* 1995). The continuous-time plant model is

$$P(s) = \frac{25(-0.4s+1)}{(s^2+3s+25)(5s+1)}$$
(51)

and the designed PID controller is

$$C(s) = 1.311 + \frac{0.431}{s} + \frac{1.048s}{1 + 12.92s}$$
(52)

The sampled-data system with the infinite-precision digital controller is stable when the sampling period  $h \le 2^3$ . The range of the sampling period tested in the simulation was  $2^3$  to  $2^{-12}$ , to cover the slow to very fast sampling conditions. For the comparison purpose, both the z and  $\delta$  based controllers were investigated in the simulation. To study the important role of the optimization algorithm employed, both the conventional Rosenbrock and advanced ASA algorithms were used in the optimization.

#### 6.1. Results for z operator based controllers

Given a sampling rate, the discrete-time plant model P(z) and the digital controller C(z) were obtained. The initial controller realization  $X_0$  was chosen to be the controllable canonical realization. The eigenvalues  $\{\lambda_i\}$  of the ideal closed-loop system and the eigenvalue sensitivity matrices  $\{\Phi_i\}$  were then computed. The optimal PID controller realizations obtained by solving the optimization problems (36) and (37) with the Rosenbrock algorithm were denoted as  $\tilde{X}_{opt1}$  and  $\tilde{X}_{opt2}$ , respectively. Similarly, the two optimal solutions of (36) and (37) obtained using the ASA algorithm were denoted as  $X_{opt1}$  and  $X_{opt2}$ , respectively. Table 1 summarizes the values of the stability lower bound measure  $\mu_1$  for different controller realizations under various sampling conditions, and table 2 lists the corresponding estimated minimum bit lengths that can guarantee the closed-loop stability for these controller realizations.

Several observations can readily be made. The results given in tables 1 and 2 show that the optimal controller realizations have much larger closed-loop stability margins than the non-optimal controllable canonical realization and require much smaller word lengths in fixed-point implementation. In the very fast sampling condition of  $h = 2^{-12}$ , the stability measure of  $X_{\text{opt2}}$  is 10<sup>5</sup> times larger than that of  $X_0$ . It can also be seen that, when the sampling rate increases, the closedloop stability measure of the z-based controller decreases considerably. This is true for non-optimal and optimal controller realizations. The ASA algorithm generally yielded better optimization results, compared with the Rosenbrock algorithm. From the results listed in table 1, it is obvious that the conventional Rosenbrock algorithm often missed the true global optimal solution, particularly under fast sampling conditions.

### 6.2. Results for $\delta$ operator-based controllers

The discrete-time plant model  $P(\delta)$  and controller  $C(\delta)$  were derived for each sampling rate. The initial

J. Wu et al.

| Stability measure $\mu_1$  |   |  |  |  |
|--|---|--|--|--|
| $X_0$  | $	ilde{X}_{opt1}$   | $	ilde{X}_{\mathrm{opt2}}$   | X <sub>opt1</sub>  | $X_{opt2}$   |
| 1.306137e-2<br>1.738083e-2<br>5.898659e-3<br>1.754786e-3<br>4.819871e-4<br>1.265127e-4<br>3.242422e-5<br>8.208513e-6<br>2.065125e-6<br>5.179179e-7<br>1.296848e-7<br>3.244692e-8<br>8.114948e-9<br>2.029139e-9<br>5.073338e-10 | 3.856 044e-2<br>1.128 260e-1<br>6.305 634e-2<br>2.805 390e-2<br>9.198 569e-3<br>2.625 682e-3<br>1.361 176e-3<br>2.757 079e-4<br>6.045 046e-5<br>1.073 104e-5<br>1.512 025e-5<br>6.614 090e-7<br>1.758 011e-7<br>3.318 801e-8<br>1.023 806e-8  | 3.893 488e-2<br>1.244 934e-1<br>6.994 177e-2<br>3.574 639e-2<br>9.684 189e-3<br>1.056 738e-2<br>8.597 592e-4<br>6.607 722e-4<br>7.753 055e-4<br>1.216 844e-3<br>2.101 092e-5<br>3.962 129e-6<br>7.370 831e-7<br>4.552 015e-7<br>7.476 508e-9 | 3.767 304e-2<br>1.495 225e-1<br>1.232 973e-1<br>5.466 344e-2<br>2.580 249e-2<br>8.901 379e-3<br>6.696 976e-3<br>2.931 593e-3<br>8.875 924e-4<br>5.955 617e-4<br>1.523 501e-5<br>8.400 932e-6<br>5.230 490e-6<br>5.204 957e-7<br>6.273 487e-8 | 3.675 970e-2<br>1.641 928e-1<br>1.273 720e-1<br>7.310 598e-2<br>3.771 688e-2<br>1.921 549e-2<br>9.719 583e-3<br>4.889 652e-3<br>2.144 777e-3<br>1.073 485e-3<br>5.331 186e-4<br>3.021 479e-4<br>1.240 600e-4<br>6.892 182e-5<br>3.090 558e-5 |
| 1.268400e-10   | 2.150 888e-9  | 2.923 607e-9   | 1.970 226e-8   | 1.327 938e-5   |
|  | $\begin{array}{c} X_0 \\ \hline 1.306137e\text{-}2 \\ 1.738083e\text{-}2 \\ 5.898659e\text{-}3 \\ 1.754786e\text{-}3 \\ 4.819871e\text{-}4 \\ 1.265127e\text{-}4 \\ 3.242422e\text{-}5 \\ 8.208513e\text{-}6 \\ 2.065125e\text{-}6 \\ 5.179179e\text{-}7 \\ 1.296848e\text{-}7 \\ 3.244692e\text{-}8 \\ 8.114948e\text{-}9 \\ 2.029139e\text{-}9 \\ 5.073338e\text{-}10 \\ 1.268400e\text{-}10 \end{array}$ | $\begin{array}{c ccccccccccccccccccccccccccccccccccc$  | $\begin{array}{c c c c c c c c c c c c c c c c c c c $   | $\begin{array}{c c c c c c c c c c c c c c c c c c c $   |

 Table 1.
 Lower-bound stability measures of different z operator based controller realizations for various sampling periods.

| o 1.      | Estimated minimum bit length $\hat{B}_{s1}^{\min}$ |                    |                    |                   |                   |
|-----------|--|--------------------|--------------------|-------------------|-------------------|
| period h  | $X_0$  | $\tilde{X}_{opt1}$ | $\tilde{X}_{opt2}$ | X <sub>opt1</sub> | X <sub>opt2</sub> |
| $2^{3}$   | 8  | 6                  | 6                  | 6                 | 6                 |
| $2^{2}$   | 7  | 5                  | 5                  | 4                 | 4                 |
| $2^{1}$   | 8  | 6                  | 4                  | 4                 | 3                 |
| $2^{0}$   | 10   | 8                  | 5                  | 6                 | 4                 |
| $2^{-1}$  | 12   | 10                 | 9                  | 7                 | 5                 |
| $2^{-2}$  | 13   | 12                 | 8                  | 8                 | 6                 |
| $2^{-3}$  | 15   | 12                 | 13                 | 8                 | 8                 |
| $2^{-4}$  | 17   | 16                 | 13                 | 9                 | 8                 |
| $2^{-5}$  | 19   | 17                 | 13                 | 11                | 9                 |
| $2^{-6}$  | 21   | 18                 | 10                 | 11                | 10                |
| $2^{-7}$  | 23   | 18                 | 18                 | 17                | 11                |
| $2^{-8}$  | 25   | 22                 | 26                 | 18                | 12                |
| $2^{-9}$  | 27   | 26                 | 23                 | 19                | 13                |
| $2^{-10}$ | 29   | 28                 | 23                 | 22                | 14                |
| $2^{-11}$ | 31   | 31                 | 29                 | 26                | 15                |
| $2^{-12}$ | 33   | 29                 | 31                 | 26                | 17                |

Table 2. Estimated minimal bit lengths of different z operator based controller realizations for various sampling periods.

controller realization  $X_{\delta,0}$  was chosen to be the 'controllable' or direct-form realization. The eigenvalues  $\{\lambda_{\delta,i}\}\)$  of the ideal closed-loop system and the eigenvalue sensitivity matrices  $\{\Phi_i\}\)$  were then calculated to form the two optimization problems, similar to (36) and (37). The optimal PID controller realizations obtained using the Rosenbrock algorithm were denoted as  $\tilde{X}_{\delta,\text{opt1}}$ and  $\tilde{X}_{\delta,\text{opt2}}$ , respectively, and the two optimal realizations obtained using the ASA algorithm were denoted as  $X_{\delta,\text{opt1}}$  and  $X_{\delta,\text{opt2}}$ , respectively. Table 3 provides the values of the stability lower bound measure for different controller realizations under various sampling conditions, and table 4 gives the corresponding estimated minimum bit lengths that can guarantee the closed-loop stability for these realizations. Both the  $\hat{B}_{s1}^{\min}$  and  $\hat{B}_{sh}^{\min}$  are listed in table 4.

The results show that the optimal  $\delta$ -based controller realization has a much better closed-loop stability margin than the non-optimal direct-form realization  $X_{\delta,0}$ , although the difference is not as great as in the case of z parameterization. The results also confirm that the  $\delta$ -based controller realizations have better stability bounds than the z-based realizations under fast sampling conditions. Increasing the sampling rate

| C   | Stability measure $\mu_1$                                    |  |  |  |  |
|---|--|--|--|--|--|
| period h                                      | $X_{\delta,0}$   | $	ilde{X}_{\delta, \mathrm{opt1}}$           | $	ilde{X}_{\delta, { m opt}2}$                               | $X_{\delta, { m opt1}}$                                      | $X_{\delta,\mathrm{opt2}}$                                   |
| $2^{3}$<br>$2^{2}$<br>$2^{1}$<br>$2^{0}$      | 1.477 681e-3<br>4.068 193e-3<br>5.081 170e-3                 | 9.853745e-3<br>6.091837e-2<br>6.838224e-2    | 9.990 982e-3<br>6.051 241e-2<br>6.964 563e-2                 | 9.852 372e-3<br>6.267 552e-2<br>6.842 345e-2                 | 9.986 133e-3<br>6.439 696e-2<br>7.051 816e-2                 |
| $2^{-1}$<br>$2^{-2}$<br>$2^{-3}$              | 5.721 692e-3<br>6.086 598e-3<br>6.279 701e-3<br>6.379 331e-3 | 7.225940e-2<br>7.370771e-2<br>7.175426e-2    | 4.360 879e-2<br>6.204 462e-2<br>6.446 224e-2<br>4.686 201e-2 | 7.136 269e-2<br>7.274 529e-2<br>7.328 164e-2<br>7.353 966e-2 | 7.310 503e-2<br>7.445 603e-2<br>7.515 015e-2<br>7.549 933e-2 |
| $\frac{1}{2^{-4}}$<br>$\frac{2^{-5}}{2^{-6}}$ | 6.429 949e-3<br>6.455 462e-3<br>6.468 270e-3                 | 7.148 362e-2<br>7.053 846e-2<br>7.404 889e-2 | 7.104 903e-2<br>6.664 081e-2<br>6.680 168e-2                 | 7.409 947e-2<br>7.393 526e-2<br>7.396 521e-2                 | 7.567 885e-2<br>7.576 799e-2<br>7.581 252e-2                 |
| $2^{-9}$<br>$2^{-8}$<br>$2^{-9}$<br>$2^{-10}$ | 6.474 687e-3<br>6.477 899e-3<br>6.479 505e-3<br>6 480 2002 2 | 7.405083e-2<br>7.081827e-2<br>7.407674e-2    | 7.118 904e-2<br>6.691 980e-2<br>6.685 483e-2<br>7.114 443e 2 | 7.397 951e-2<br>7.417 212e-2<br>7.411 691e-2<br>7.412 070e 2 | 7.583 418e-2<br>7.584 603e-2<br>7.585 130e-2<br>7.585 433e-2 |
| $2^{-11} 2^{-12}$                             | 6.480 912e-3<br>6.480 912e-3                                 | 7.004 170e-2<br>7.404 690e-2<br>7.064 419e-2 | 7.104 011e-2<br>6.683 266e-2                                 | 7.413 070e-2<br>7.419 079e-2<br>7.422 571e-2                 | 7.585 577e-2<br>7.585 604e-2                                 |

Table 3. Lower-bound stability measures of different  $\delta$  operator based controller realizations for various sampling periods.

| C1'  | Estimated minimum bit length $(\hat{B}_{s1}^{\min}, \hat{B}_{sh}^{\min})$ |  |   |  |   |
|--|---|--|---|--|---|
| period h   | $X_{\delta,0}$  | $	ilde{X}_{\delta, { m opt1}}$   | $	ilde{X}_{\delta, \mathrm{opt2}}$  | $X_{\delta, \mathrm{opt1}}$  | $X_{\delta,\mathrm{opt2}}$  |
| $2^{3} \\ 2^{2} \\ 2^{1} \\ 2^{0} \\ 2^{-1} \\ 2^{-2} \\ 2^{-3} \\ 2^{-3} \\ 2^{-4} \\ 2^{-5} \\ 2^{-6} \\ 2^{-7} \\ 2^{-8} \\ 2^{-9} \\ 2^{-10} \\ 2^{-11} \\ 2^{-12} $ | 11, 12<br>9, 9<br>8, 8<br>8, 8<br>8, 8<br>8, 8<br>8, 8<br>8, 8<br>8, 8    | 8, 9<br>6, 6<br>4, 4<br>5, 5<br>5, 5<br>5, 5<br>4, 4<br>5, 6<br>4, 6<br>5, 8<br>4, 8<br>4, 9<br>4, 10<br>4, 11<br>4, 12<br>4, 13 | 8, 9<br>6, 6<br>4, 4<br>7, 7<br>6, 6<br>5, 5<br>7, 7<br>4, 5<br>5, 7<br>5, 8<br>4, 8<br>5, 10<br>4, 10<br>4, 11<br>4, 12<br>4, 13 | 8, 9<br>5, 5<br>4, 4<br>4, 4<br>4, 4<br>4, 4<br>4, 4<br>4, 5<br>4, 6<br>4, 7<br>4, 8<br>4, 9<br>4, 10<br>4, 11<br>4, 12<br>4, 13 | $\begin{array}{c} 8, 9\\ 5, 5\\ 4, 4\\ 4, 4\\ 4, 4\\ 4, 4\\ 4, 4\\ 4, 5\\ 4, 6\\ 4, 7\\ 4, 8\\ 4, 9\\ 4, 10\\ 4, 11\\ 4, 12\\ 4, 13\end{array}$ |

Table 4. Estimated minimal bit lengths of different  $\delta$  operator based controller realizations for various sampling periods.  $\hat{B}_{sh}^{\min}$  is estimated from  $\mu_1$  and the realization, and  $\hat{B}_{sh}^{\min}$  is the modified estimate taking into account the implementation of h.

leads to a slightly improved stability margin for the  $\delta$ -based controller realization. This is in contrast to the *z*-based controller realization, which has considerably degraded stability margin when the sampling rate increases. Although the estimated minimum bit length based on  $\mu_1$  and the realization is consistently 4 for various sampling conditions, the modified estimate of the minimum bit length is larger and increases as the sampling rate increases. Even taking this into account, however, the estimate of the minimum bit length is still smaller than that of the corresponding *z*-based controller.

#### 7. Conclusions

In this paper we have proposed a new optimal procedure for the sensitivity analysis of closed-loop stability, subject to FWL implemented controller coefficients. It has been shown that the optimal realization of finite-precision digital controllers can be interpreted as a constrained optimization problem. In particular, for finite-precision PID controllers, the optimization can be decoupled into two unconstrained optimization problems. An efficient optimization approach has been developed for solving this optimal FWL PID controller realization problem. The approach is equally valid for the digital controllers based on either z or  $\delta$  operator parameterization.

The theoretical results have been verified using a numerical example based on an IFAC benchmark PID control system. The results obtained demonstrate that the proposed approach greatly improves the stability robustness with minimum word length characteristics, compared to non-optimal realizations. The important role of an efficient global optimization method in searching for the true optimal controller realization has also been highlighted in the numerical example. The simulation study also confirms that the  $\delta$ -based controller has clear advantages over the z-based controller in FWL implementation, particularly under fast sampling conditions.

Future work will investigate the extension of the efficient method for obtaining the FWL PID controller realizations presented in this study to FWL higher-order controller realizations. Ongoing work will also explore the integration of the proposed optimization procedure with the closed-loop controller performance and the sparseness consideration of controller realizations. This will provide a multi-objective framework to develop the optimal finite-precision controller realization that possesses the optimal trade-off between minimal computational requirements, improved performance and stability robustness.

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#### Appendix

Define the  $n \times n$  diagonal matrix set

$$_{n} \triangleq \{ U = \text{diag}(u_{1}, u_{2}, \dots, u_{n}) : u_{i} \in \{-1, 1\} \\ \forall i \in \{1, \dots, n\} \}$$
 (53)

From the definition of  $\|M\|_{s}$  (29), we have

**Lemma 3:**  $\forall M \in c^{m \times n}$ ,  $U_1 \in m$  and  $U_2 \in n$ 

 $\| U_1 M \|_s = \| M \|_s \quad \text{and} \quad \| M U_2 \|_s = \| M \|_s \quad (54)$ 

$$\zeta_{0} \triangleq \begin{bmatrix} t_{1} & t_{2} \\ t_{3} & t_{4} \end{bmatrix} : t_{1} \in \pi, t_{2} \in \pi, t_{3} \in \pi, t_{4} \in \pi, t_{1}t_{4} - t_{2}t_{3} \neq 0$$

$$\zeta_{1} \triangleq \begin{bmatrix} t_{1} & t_{2} \\ 0 & t_{4} \end{bmatrix} : t_{1} \in \pi, t_{2} \in \pi, t_{4} \in \pi, t_{1}t_{4} \neq 0$$

$$\zeta_{2} \triangleq \begin{bmatrix} t_{1} & t_{2} \\ t_{3} & t_{4} \end{bmatrix} : t_{1} \in \pi, t_{2} \in \pi, t_{3} \in \pi, t_{4} \in \pi, t_{3} \neq 0, t_{1}t_{4} - t_{2}t_{3} \neq 0$$

$$(55)$$

Construct the optimization problems

$$v_{1} \triangleq \min_{\tau \in \zeta_{1}} \max_{i \in \{1,...,m+2\}} \left\| \begin{bmatrix} 1 & 0 \end{bmatrix}^{-1} \begin{bmatrix} 1 & 0 \end{bmatrix}^{-1} \\ 0 & \tau^{-1} & 0 \end{bmatrix}_{s} (56)$$

and

$$v_{2} \triangleq \min_{\tau \in \zeta_{2}} \max_{i \in \{1, \dots, m+2\}} \left\| \begin{bmatrix} 1 & 0 \end{bmatrix}_{\tau} \begin{bmatrix} 1 & 0 \end{bmatrix}_{\tau} \begin{bmatrix} 1 & 0 \end{bmatrix}_{r} \\ 0 & \tau \end{bmatrix}_{s} (57)$$

Obviously  $\zeta_0 = \zeta_1 \cup \zeta_2$  and, therefore,  $v = \min \{v_1, v_2\}$ . Define the function sgn(')

$$sgn(x) = \begin{cases} 1, & x \ge 0 \\ -1, & x < 0 \end{cases}$$
(58)

Consider the optimization problem (56). Utilizing Lemma 3,  $\forall \tau \in \zeta_1$  and  $\forall i \in \{1, \dots, m+2\}$  we have

$$\begin{vmatrix} 1 & 0 & 0 & 1 & 0 \\ 0 & \tau^{T} & \Phi_{i} & 0 & \tau^{-T} \\ 0 & \tau^{T} & \Phi_{i} & 0 & \tau^{-T} \\ \end{vmatrix}_{s}^{s}$$

$$= \begin{vmatrix} 1 & 0 & 0 & 1 & 1 & 0 & 0 \\ 0 & t_{2} & t_{4} & 0 & -t_{2}/(t_{1}t_{4}) & 1/t_{4} \\ 0 & t_{2} & t_{4} & 0 & -t_{2}/(t_{1}t_{4}) & 1/t_{4} \\ \end{vmatrix}_{s}^{s}$$

$$= \begin{vmatrix} 1 & 0 & 0 & 1 & 1 & 0 & 0 \\ \frac{1}{\tau + t_{1}t_{4}} & 0 & sgn(t_{1}) & 0 & \frac{1}{\tau} & 0 & t_{1} & 0 \\ 0 & 0 & sgn(t_{4}) & 0 & t_{2} & t_{4} \\ \frac{1}{\tau + t_{1}t_{4}} & 0 & 0 & \frac{1}{\tau} \\ 0 & 0 & sgn(t_{4}) & 0 & t_{2} & t_{4} \\ \frac{1}{\tau + t_{1}t_{4}} & 0 & 0 & \frac{1}{\tau} \\ \frac{1}{\tau + t_{1}t_{4}} & 0 & 0 & \frac{1}{\tau} \\ \frac{1}{\tau + t_{1}t_{4}} & 0 & 0 & \frac{1}{\tau} \\ \frac{1}{\tau + t_{1}t_{4}} & 0 & 0 & \frac{1}{\tau} \\ \frac{1}{\tau + t_{1}t_{4}} & 0 & 0 & \frac{1}{\tau} \\ \frac{1}{\tau + t_{1}t_{4}} & 0 & 0 & \frac{1}{\tau} \\ 0 & sgn(t_{4}) & t_{2}/\tau + t_{1}t_{4} & \frac{1}{\tau} \\ \frac{1}{\tau + t_{1}t_{4}} & 0 & 0 & \frac$$

Define

1340

$$x = \frac{|t_1|/|t_4| \in (0, +\infty)}{|t_1t_4| \in (-\infty, +\infty)}$$

$$y = \operatorname{sgn}(t_4) \frac{t_2}{|t_1t_4|} \in (-\infty, +\infty)$$

$$W = \frac{1}{|t_1t_4|} \in (0, +\infty)$$
(60)

Then

 $f_1(z)$ 

$$\begin{aligned}
x, y, w &\triangleq \max_{i \in \{1, \dots, m+2\}} \left\| \begin{bmatrix} w & 0 & 0 \\ 0 & x & 0 \\ 0 & y & 1/x \end{bmatrix} \\
& & \begin{bmatrix} 1/w & 0 & 0 \\ 0 & y & 1/x \end{bmatrix} \\
& & & \begin{bmatrix} 1/w & 0 & 0 \\ 0 & -y & x \end{bmatrix} \\
& & & \\ & & & & \\ & & & \\ & & & \\ & & & \\ & & & & \\ & & & \\ & & & & \\ & & &$$

and

$$\begin{aligned}
\nu_{1} &\triangleq \min_{\tau \in \zeta_{1}} \max_{i \in \{1, \dots, m+2\}} \left\| \begin{bmatrix} 1 & 0 \end{bmatrix} \begin{bmatrix} 1 & 0 \end{bmatrix} \\ 0 & \tau \end{bmatrix} \right\|_{s} \\
&= \min_{\substack{X \in \{0, +\infty\} \\ y \in (-\infty, +\infty) \\ W \in \{0, +\infty\} \\ W \in \{0, +\infty\} \\ \end{array}} f_{1}(x, y, w) 
\end{aligned}$$
(62)

If  $v = v_1$  and  $(x_{opt1}, y_{opt1}, w_{opt1})$  is the solution of the optimization problem (62)

н.

$$v = v_{1} = \max_{i \in \{1,...,m+2\}} \left\| \begin{vmatrix} w_{opt1} & 0 & 0 \\ 0 & x_{opt1} & 0 \end{vmatrix} \right|_{s}^{1}$$

$$\times \Phi_{i} \begin{vmatrix} 0 & 1/x_{opt1} & 0 \\ 0 & -y_{opt1} & x_{opt1} \end{vmatrix} = \max_{i \in \{1,...,m+2\}} \left\| \frac{1}{w_{opt1}} \begin{vmatrix} w_{opt1} & 0 & 0 \\ 0 & y_{opt1} & x_{opt1} \end{vmatrix} \right\|_{s}^{1}$$

$$= \max_{i \in \{1,...,m+2\}} \left\| \frac{1}{w_{opt1}} \begin{vmatrix} 0 & x_{opt1} & 0 \\ 0 & y_{opt1} & 1/x_{opt1} \end{vmatrix}$$

$$\times \Phi_{i} \begin{vmatrix} 0 & 1/x_{opt1} & 0 \\ 0 & -y_{opt1} & x_{opt1} \end{vmatrix} \right\|_{s}^{1}$$
(63)

which means that

$$\tau_{opt} = \frac{1}{w_{opt1}} \begin{bmatrix} x_{opt1} & y_{opt1} \\ 0 & 1/x_{opt1} \end{bmatrix}$$
(64)

is the optimal solution of the problem (33).

By considering (57) in a similar way, we can prove the rest of Theorem 2.  $\Box$ 

#### References

- ÅSTRÖM, K. J., and WITTENMARK, K., 1989, *Computer Controlled Systems, Theory and Design* (Englewood Cliffs, NJ: Prentice Hall).
- BEVERIDGE, G. S. G., and SCHECHTER, R. S., 1970, *Optimization: Theory and Practice* (McGraw-Hill).
- CHEN, S., LUK, B. L., and LIU, Y., 1998, Application of adaptive simulated annealing to blind channel identification with HOC fitting. *Electronics Letters*, 34, 234–235.
- CHEN, T., and FRANCIS, B. A., 1991, Input–output stability of sampled-data systems. *IEEE Transactions on Automatic Control*, 36, 50–58.
- DAVIS, L. (Ed.), 1991, *Handbook of Genetic Algorithms* (Van Nostrand Reinhold).
- DIXON, L. C. W., 1972, Nonlinear Optimisation (London: The English Universities Press Ltd).
- FIALHO, I. J., and GEORGIOU, T. T., 1994, On stability and performance of sampled-data systems subject to wordlength constraint. *IEEE Transactions on Automatic Control*, **39**, 2476–2481.
- GEVERS, M., and LI, G., 1993, *Parameterizations in Control, Estimation and Filtering Problems: Accuracy Aspects* (London: Springer Verlag).
- GOLDBERG, D. E., 1989, *Genetic Algorithms in Search, Optimization, and Machine Learning* (Reading, MA.: Addison-Wesley).
- INGBER, L., 1996, Adaptive simulated annealing (ASA): lessons learned. *Journal of Control and Cybernetics*, 25, 33–54.
- INGBER, L., and ROSEN, B. E., 1992, Genetic algorithms and very fast simulated reannealing: a comparison. *Mathematical and Computer Modelling*, 16, 87–100.
- ISTEPANIAN, R. H., 1997, Implementation issues for discrete PID algorithms using shift and delta operators parameterizations. In *Proceedings of the 4th IFAC Workshop Algorithms and Architectures for Real-Time Control* (Vilamoura, Portugal), pp. 117–122.
- ISTEPANIAN, R. H., LI, G., WU, J., and CHU, J., 1998 a, Analysis of sensitivity measures of finite-precision digital controller structures with closed-loop stability bounds. *IEE Proceedings of the Control Theory and Applications*, 145, 472–478.
- ISTEPANIAN, R. H., PRATT, I., GOODALL, R., and JONES, S., 1996, Effect of fixed point parameterization on the performance of active suspension control systems. In *Proceedings* of the 13th IFAC World Congress (San Francisco, USA), pp. 291–295.
- ISTEPANIAN, R. H., WU, J., WHIDBORNE, J. F., YAN, J., and SALCUDEAN, S. E., 1998 b, Finite-word-length stability issues of teleoperation motion-scaling control system. In *Proceedings of the UKACC Control'98* (Swansea, UK), pp. 1676–1681.
- KOWALIK, J., and OSBORNE, M. R., 1968, *Methods for* Unconstrained Optimization Problems (New York: Elsevier).
- Li, G., 1998, On the structure of digital controllers with finite word length consideration. *IEEE Transactions on Automatic Control*, 43, 689–693.
- Li, G., and GEVERS, M., 1996, On the structure of digital controllers in sampled data systems with FWL consideration. In *Proceedings of the 35th IEEE Conference on Decision* and Control (Kobe, Japan), pp. 919–920.

- MADIEVSKI, A. G., ANDERSON, B. D. O., and GEVERS, M., 1995, Optimum realizations of sampled-data controllers for FWL sensitivity minimization. *Automatica*, **31**, 367–379.
- Man, K. F., Tang, K. S., Kwong, S., and Halang, W. A., 1997, *Genetic Algorithms for Control and Signal Processing* (London: Springer).
- MASTEN, M. K., and PANAHI, I., 1997, Digital signal processors for modern control systems. *Control Engineering Practice*, **5**, 449–458.
- MIDDLETON, R. H., and GOODWIN, G. C., 1990, *Digital Control and Estimation: A Unified Approach* (New Jersey: Prentice Hall).
- MORONEY, P., WILLSKY, A. S., and HOUPT, P. K., 1980, The digital implementation of control compensators: the coefficient wordlength issue. *IEEE Transactions on Automatic Control*, **25**, 621–630.
- NEUMAN, C. P., 1993 a, Transformations between delta and forward shift operator transfer function models. *IEEE Transactions on System Man and Cybernetics*, 23, 295–296.
- NEUMAN, C. P., 1993 b, Properties of the delta operator model of dynamic physical systems. *IEEE Transactions on System Man and Cybernetics*, 23, 296–301.

- RAD, A. B., and Lo, W. L., 1994, Predictive PI controller. International Journal of Control, 60, 953–975.
- Rosen, B. E., 1997, Rotationally parameterized very fast simulated reannealing. Submitted to *IEEE Transactions on Neural Networks*, 1997.
- WHIDBORNE, J. F., MURAD, G., GU, D. W., and POSTLETHWAITE, I., 1995, Robust control of an unknown plant—the IFAC93 benchmark. *International Journal of Control*, 61, 589–640.
- WU, J., ISTEPANIAN, R. H., CHEN, S., CHU, J., and WHIDBORNE, J. F., 1999 a, Optimizing stability bounds of finite-precision controller structures for sampled-data systems in the delta operator domain. Submitted to *IEE Proceedings of the Control Theory and Applications*.
- WU, J., ISTEPANIAN, R. H., CHU, J., WHIDBORNE, J. F., CHEN, S., and HU, J., 1999 b, Stability issues of finite precision controller structures using the delta operator for sampled data systems. To be presented at 14th IFAC World Congress (Beijing, China), July 4–9.