Comparative study on optimizing closed-loop stability bounds of finite-precision controller structures with shift and delta operators

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Abstract

The paper analyzes the sensitivity of closed-loop stability with respect to (w.r.t.) finite word length (FWL) effects in the implementation of the controller coefficients using both the shift and delta operator parameterizations. A unified approach is established to optimize a closed-loop stability lower bound for FWL controller structures. This provides a general framework to compare the FWL closed-loop stability characteristics of the controller structures based on the shift and delta operators, respectively. Two numerical examples are given, and the simulation results show that the optimal FWL controller realizations with the delta operator have better closed-loop stability margins than those with the shift operator. © 2000 Elsevier Science B.V. All rights reserved.

Keywords: Sampled-data system; Digital controller; Finite word length; Stability; Shift and delta operators; Optimization

1. Introduction

Controller implementations with fixed-point arithmetic offer the advantages of speed, memory space and simplicity over floating-point arithmetic. However, a designed stable closed-loop system may become unstable when the infinite-precision controller is implemented using a fixed-point processor due to FWL effects. The "robustness" of closed-loop stability w.r.t. controller parameter perturbations therefore is a critical issue in fixed-point implementations of digital controllers. A discrete-time system can be described and realized either with the usual shift z operator or the delta δ operator [12]. Two major advantages are claimed for the use of δ operator realization: a theoretically unified formulation of continuous-time and discrete-time systems; and better numerical properties in FWL implementations [8].

The issues of FWL controller realizations in the z domain have been addressed [7,11,10]. Investigations on finite-precision controller realizations with the z operator parameterization and relevant optimization problems

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have mainly been based on the sensitivity of the closed-loop pole positions w.r.t. FWL effects [11,10,3]. This approach has recently been extended to study the closed-loop stability issues of FWL controller structures using the δ operator formulation [14,4]. In this paper, we provide a comparative study on optimizing closed-loop stability bounds of finite-precision controller structures with the z and δ operator parameterizations.

We present a unified approach for analyzing the sensitivity of closed-loop stability w.r.t. FWL effects for both the z and δ parameterizations. A tractable lower-bound stability measure is provided. The optimal FWL controller realization, which maximizes this lower-bound stability measure, can be obtained by solving a constrained optimization problem. For the PID controller, this constrained optimization can be decoupled into two unconstrained ones. As the optimization criteria are non-smooth and non-convex, a conventional optimization algorithm [1,6] may become trapped at some local minima and give misleading results. It is therefore critical to employ a global optimization method, such as genetic algorithms or adaptive simulated annealing (ASA) [9,2], in our comparative study. We adopt the ASA because of its simplicity in programming.

Two numerical examples, an IFAC benchmark PID control system [13] and a control system with a high-order controller [5], are used in the comparative study. The simulation results demonstrate that the δ-based controller realization has better stability robustness to FWL effects than the z-domain approach, particularly for fast sampling. When the sampling rate increases, the closed-loop stability of the z-based FWL controller deteriorates significantly while the δ-based FWL controller is actually improved slightly.

2. Closed-loop stability with FWL controller structures

We first define a "generalized" operator ρ for discrete-time systems. It is understood that $\rho = z$ or δ, depending on which operator is actually used. The δ operator is given by [12]

$$\delta = \frac{z-1}{h}, \tag{1}$$

where h is the sampling period. Given a transfer function $G(\rho)$, the state-space model is

$$\rho x(k) = A_{\rho,G} x(k) + B_{\rho,G} u(k),$$
$$y(k) = C_{\rho,G} x(k) + D_{\rho,G} u(k). \tag{2}$$

The state-space realization of an input–output transfer function is not unique. Define

$$S_\rho \triangleq \{(A_{\rho,G}, B_{\rho,G}, C_{\rho,G}, D_{\rho,G}): G(\rho) = C_{\rho,G}(\rho I - A_{\rho,G})^{-1} B_{\rho,G} + D_{\rho,G}\}. \tag{3}$$

Then if $(A_{\rho,G}, B_{\rho,G}, C_{\rho,G}, D_{\rho,G}) \in S_\rho$, $(\mathcal{T}^{-1} A_{\rho,G} \mathcal{T}, \mathcal{T}^{-1} B_{\rho,G}, C_{\rho,G} \mathcal{T}, D_{\rho,G}) \in S_\rho$ for any non-singular $\mathcal{T}$. The relationships relate the two state-space representations in the z and δ operators are

$$A_{z,G} = hA_{\delta,G} + I, \quad B_{z,G} = hB_{\delta,G}, \quad C_{z,G} = C_{\delta,G}, \quad D_{z,G} = D_{\delta,G}. \tag{4}$$

Let $\{\lambda_{\rho,i}\}$ be the eigenvalues of $A_{\rho,G}$. The following lemma relates $\{\lambda_{\delta,i}\}$ to $\{\lambda_{z,i}\}$.

Lemma 1. $\lambda_{z,i} = 1 + h\lambda_{\delta,i}, \forall i.$

It is well known that the discrete-time system $(A_{z,G}, B_{z,G}, C_{z,G}, D_{z,G})$ is stable if and only if all the eigenvalues $|\lambda_{z,i}| < 1$. From Lemma 1, we have the condition of the stability of the discrete-time system described with the δ operator.

Lemma 2. *The discrete-time system* $(A_{\delta,G}, B_{\delta,G}, C_{\delta,G}, D_{\delta,G})$ *is stable if and only if*

$$\left| \lambda_{\delta,i} + \frac{1}{h} \right| < \frac{1}{h}, \quad \forall i. \tag{5}$$

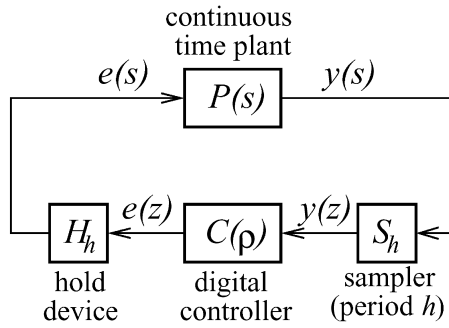Consider the sampled-data system depicted in Fig. 1, where the continuous-time plant $P(s)$ is strictly proper.

Fig. 1. Sampled-data system with digital controller realization, where $\rho = z$ or δ.

The discrete-time plant $P(\rho) = S_h P(s) H_h$ has a state-space realization ($A_{\rho,P} \in \mathbb{R}^{m \times m}, B_{\rho,P} \in \mathbb{R}^{m \times l}, C_{\rho,P} \in \mathbb{R}^{q \times m}, 0$). The controller $C(\rho)$ has a state-space realization ($A_{\rho,C} \in \mathbb{R}^{n \times n}, B_{\rho,C} \in \mathbb{R}^{n \times q}, C_{\rho,C} \in \mathbb{R}^{l \times n}, D_{\rho,C} \in \mathbb{R}^{l \times q}$). The corresponding state-space realization ($\bar{A}_\rho, \bar{B}_\rho, \bar{C}_\rho, \bar{D}_\rho$) of the closed-loop system is

$$\bar{A}_\rho = \begin{bmatrix} A_{\rho,P} & 0 \\ 0 & 0 \end{bmatrix} + \begin{bmatrix} B_{\rho,P} & 0 \\ 0 & I_n \end{bmatrix} \begin{bmatrix} D_{\rho,C} & C_{\rho,C} \\ B_{\rho,C} & A_{\rho,C} \end{bmatrix} \begin{bmatrix} C_{\rho,P} & 0 \\ 0 & I_n \end{bmatrix}$$
$$= M_0 + M_1 X_\rho M_2 = \bar{A}_\rho(X_\rho), \tag{6}$$

$$\bar{B}_\rho = \begin{bmatrix} B_{\rho,P} \\ 0 \end{bmatrix}, \quad \bar{C}_\rho = [C_{\rho,P}\ 0], \quad \bar{D}_\rho = 0, \tag{7}$$

where

$$X_\rho = \begin{bmatrix} D_{\rho,C} & C_{\rho,C} \\ B_{\rho,C} & A_{\rho,C} \end{bmatrix} = \begin{bmatrix} p_1 & p_2 & \cdots & p_{q+n} \\ p_{q+n+1} & p_{q+n+2} & \cdots & p_{2(q+n)} \\ \vdots & \vdots & \cdots & \vdots \\ p_{(l+n-1)(q+n)+1} & p_{(l+n-1)(q+n)+2} & \cdots & p_{(l+n)(q+n)} \end{bmatrix} \tag{8}$$

is the controller matrix. Denote the eigenvalues of $\bar{A}_\rho(X_\rho)$ by $\{\bar{\lambda}_{\rho,i}, 1 \leqslant i \leqslant m + n\}$. We define the "stability margin" of $\bar{\lambda}_{\rho,i}$ as

$$StMa(\bar{\lambda}_{\rho,i}) = \begin{cases} 1 - |\bar{\lambda}_{z,i}|, & \rho = z, \\ \frac{1}{h} - |\bar{\lambda}_{\delta,i} + \frac{1}{h}|, & \rho = \delta. \end{cases} \tag{9}$$

Let $C(\rho)$ be chosen to make the closed-loop system stable. Then $StMa(\bar{\lambda}_{\rho,i}) > 0, \forall i$.

When the realization ($A_{\rho,C}, B_{\rho,C}, C_{\rho,C}, D_{\rho,C}$) is implemented with a fixed-point processor, X_ρ is perturbed into $X_\rho + \Delta X_\rho$ due to the FWL effects, where

$$\Delta X_\rho = \begin{bmatrix} \Delta p_1 & \Delta p_2 & \cdots & \Delta p_{q+n} \\ \Delta p_{q+n+1} & \Delta p_{q+n+2} & \cdots & \Delta p_{2(q+n)} \\ \vdots & \vdots & \cdots & \vdots \\ \Delta p_{(l+n-1)(q+n)+1} & \Delta p_{(l+n-1)(q+n)+2} & \cdots & \Delta p_N \end{bmatrix} \tag{10}$$

and $N = (l + n)(q + n)$. Each element of ΔX_ρ is bounded by $\varepsilon/2$, that is,

$$\mu(\Delta X_\rho) \triangleq \max_{1 \leqslant i \leqslant N} |\Delta p_i| \leqslant \frac{\varepsilon}{2}. \tag{11}$$

For a fixed-point processor of B_s bits, $\varepsilon = 2^{-(B_s - B_X)}$, and 2^{B_X} is a normalization factor such that the absolute value of each element of $2^{-B_X} X_\rho$ is not larger than 1. With the perturbation ΔX_ρ, $\bar{\lambda}_{\rho,i}$ is moved to $\tilde{\lambda}_{\rho,i}$. The

sampled-data system will be unstable if there exists $i \in \{1, \ldots, m+n\}$ such that $StMa(\tilde{\lambda}_{\rho,i}) \leqslant 0$. To see when the round off error will cause the closed-loop system to become unstable, define the stability measure

$$\mu_{\rho,0}(X_\rho) \triangleq \inf\{\mu(\Delta X_\rho) : \bar{A}_\rho(X_\rho + \Delta X_\rho) \text{ is unstable}\}. \tag{12}$$

From this definition, it is obvious that:

Proposition 1. $\bar{A}_\rho(X_\rho + \Delta X_\rho)$ *is stable if* $\mu(\Delta X_\rho) < \mu_{\rho,0}(X_\rho)$.

The large $\mu_{\rho,0}(X_\rho)$ is, the bigger FWL error the closed-loop can tolerate. For a given X_ρ, the closed-loop stability is guaranteed if the FWL error lies in the bounded region:

$$\mathscr{Q}(X_\rho) \triangleq \{\Delta X_\rho : \mu(\Delta X_\rho) < \mu_{\rho,0}(X_\rho)\}. \tag{13}$$

Thus, $\mu_{\rho,0}(X_\rho)$ quantifies the stability robustness of the realization X_ρ to the FWL effects. However, computing explicitly the value of $\mu_{\rho,0}(X_\rho)$ is an unsolved open problem. For the computational purpose, we define the following tractable alternative:

$$\mu_{\rho,1}(X_\rho) = \min_{1 \leqslant i \leqslant m+n} \frac{StMa(\bar{\lambda}_{\rho,i})}{\sum_{j=1}^{N} |\partial \bar{\lambda}_{\rho,i} / \partial p_j|_{X_\rho}|}. \tag{14}$$

Proposition 2. *There exists a rather large* $\kappa > 0$ *such that*

$$\forall \Delta X_\rho \in \mathscr{P}(X_\rho) \triangleq \{\Delta X_\rho : \mu(\Delta X_\rho) \leqslant \kappa\}, \tag{15}$$

$\bar{A}_\rho(X_\rho + \Delta X_\rho)$ *is stable if* $\mu(\Delta X_\rho) < \mu_{\rho,1}(X_\rho)$.

The proof of this proposition for the case of $\rho = \delta$ is given in [4]. The requirement of $\Delta X_\rho \in \mathscr{P}(X_\rho)$ is not over restricted, since $\mathscr{P}(X_\rho)$ covers at least a large part of $\mathscr{Q}(X_\rho)$.

Corollary 1. $\mu_{\rho,1}(X_\rho) \leqslant \mu_{\rho,0}(X_\rho)$ *if*

$$\mu_{\rho,0}(X_\rho) < \inf_{\Delta X_\rho \notin \mathscr{P}(X_\rho)} \mu(\Delta X_\rho).$$

Thus, $\mu_{\rho,1}(X_\rho)$ can be considered as a lower bound of $\mu_{\rho,0}(X_\rho)$, provided that $\mu_{\rho,0}(X_\rho)$ is small enough. The following theorem shows that $\mu_{\rho,1}(X_\rho)$ can be computed easily.

Theorem 1. *Let* $\{\bar{\lambda}_{\rho,i}, \ 1 \leqslant i \leqslant m+n\}$ *be the eigenvalues of* $\bar{A}_\rho(X_\rho)$, *and* $\mathbf{x}_i$ *and* $\mathbf{y}_i$ *be the right and reciprocal left eigenvectors corresponding to* $\bar{\lambda}_{\rho,i}$, *respectively. Then*

$$\frac{\partial \bar{\lambda}_{\rho,i}}{\partial X_\rho} = \begin{bmatrix} \dfrac{\partial \bar{\lambda}_{\rho,i}}{\partial p_1} & \dfrac{\partial \bar{\lambda}_{\rho,i}}{\partial p_2} & \cdots & \dfrac{\partial \bar{\lambda}_{\rho,i}}{\partial p_{q+n}} \\ \dfrac{\partial \bar{\lambda}_{\rho,i}}{\partial p_{q+n+1}} & \dfrac{\partial \bar{\lambda}_{\rho,i}}{\partial p_{q+n+2}} & \cdots & \dfrac{\partial \bar{\lambda}_{\rho,i}}{\partial p_{2(q+n)}} \\ \vdots & \vdots & \cdots & \vdots \\ \dfrac{\partial \bar{\lambda}_{\rho,i}}{\partial p_{(l+n-1)(q+n)+1}} & \dfrac{\partial \bar{\lambda}_{\rho,i}}{\partial p_{(l+n-1)(q+n)+2}} & \cdots & \dfrac{\partial \bar{\lambda}_{\rho,i}}{\partial p_N} \end{bmatrix} = M_1^{\mathrm{T}} \mathbf{y}_i^* \mathbf{x}_i^{\mathrm{T}} M_2^{\mathrm{T}}, \tag{16}$$

where T *denotes the transpose operation, and* * *the conjugate operation.*

The proof of this theorem is identical to the special case of $\rho = z$ given in [11,10].

How "robust" a controller is to the FWL effects can also be viewed from another angle. Let $B_s^{\min}$ be the smallest word length that can guarantee the closed-loop stability. It would be highly desirable to know $B_s^{\min}$

for a given realization X_ρ. However, except in simulation, it is impossible to test the closed-loop system by reducing B_s until it becomes unstable. An estimate of B_s^{min} is given by

$$\hat{B}_s^{min} = \text{Int}[-\log_2(\mu_{\rho,1}(X_\rho))] - 1 + B_X, \tag{17}$$

where $\text{Int}[x]$ rounds x to the nearest integer and $\text{Int}[x] \geqslant x$. From (11) and Proposition 2, it can be seen that the closed-loop system is stable when X_ρ is implemented with a fixed-point processor of at least $\hat{B}_s^{min}$ bits.

It is worth emphasizing a practical consideration on the FWL implementation of δ-based controllers. The state-space equation of the δ-based controller

$$\delta x(k) = A_{\delta,C} x(k) + B_{\delta,C} u(k) \tag{18}$$

is realized using

$$x(k+1) = x(k) + h(A_{\delta,C} x(k) + B_{\delta,C} u(k)). \tag{19}$$

The sampling period h should be implemented exactly without any FWL errors. Otherwise, analysis based on X_δ may not be valid. Assume that h can be realized exactly by B_h bits with the integer part of h requiring B_{hI} bits and the fractional part of h requiring B_{hF} bits. A modified estimate of B_s^{min} is

$$\hat{B}_{sh}^{min} = \max\{B_{hI}, B_X\} + \max\{B_{hF}, \hat{B}_s^{min} - B_X\}. \tag{20}$$

This modified estimate can be pessimistic, but the estimate (17) is not always correct for δ realizations. Controllers based on the z operator do not have this problem.

3. Optimal FWL controller realizations

From the previous discussions, we know that there are different realizations X_ρ for a given $C(\rho)$, and the stability measure $\mu_{\rho,1}(X_\rho)$ is a function of the realization. It is of practical importance to find a realization such that $\mu_{\rho,1}(X_\rho)$ is maximized. Such a realization is optimal in the sense that it has maximum stability robustness to FWL effects. The digital controller implemented with an optimal realization means that the stability of the closed-loop system is guaranteed with a minimum hardware requirement in terms of word length. Given an initial realization $X_{\rho,0}$ of $C(\rho)$,

$$X_{\rho,0} = \begin{bmatrix} D_{\rho,C}^0 & C_{\rho,C}^0 \\ B_{\rho,C}^0 & A_{\rho,C}^0 \end{bmatrix}, \tag{21}$$

any realization of $C(\rho)$ can be expressed as

$$X_{\rho,\mathcal{T}} \triangleq \begin{bmatrix} I_l & 0 \\ 0 & \mathcal{T}^{-1} \end{bmatrix} X_{\rho,0} \begin{bmatrix} I_q & 0 \\ 0 & \mathcal{T} \end{bmatrix}, \tag{22}$$

where $\mathcal{T} \in \mathbb{R}^{n \times n}$ and $\det(\mathcal{T}) \neq 0$. From (6), the transition matrix of the closed-loop system is

$$\bar{A}_\rho(X_{\rho,\mathcal{T}}) = \begin{bmatrix} I_m & 0 \\ 0 & \mathcal{T}^{-1} \end{bmatrix} \bar{A}_\rho(X_{\rho,0}) \begin{bmatrix} I_m & 0 \\ 0 & \mathcal{T} \end{bmatrix}. \tag{23}$$

Since the eigenvalues of $\bar{A}_\rho(X_{\rho,0})$ and $\bar{A}_\rho(X_{\rho,\mathcal{T}})$ are identical, the same symbol $\bar{\lambda}_{\rho,i}$ is used to denote their i-th eigenvalue. From (23), applying Theorem 1 results in

$$\left. \frac{\partial \bar{\lambda}_{\rho,i}}{\partial X} \right|_{X = X_{\rho,\mathcal{T}}} = \begin{bmatrix} I_l & 0 \\ 0 & \mathcal{T}^T \end{bmatrix} \left. \frac{\partial \bar{\lambda}_{\rho,i}}{\partial X} \right|_{X = X_{\rho,0}} \begin{bmatrix} I_q & 0 \\ 0 & \mathcal{T}^{-T} \end{bmatrix}. \tag{24}$$

From (14), (16) and (24), we can define the optimal controller realization as the solution of the following maximization problem:

$$\varphi_\rho \triangleq \max_{\substack{\mathcal{T} \in \mathbb{R}^{n \times n} \\ \det(\mathcal{T}) \neq 0}} \mu_{\rho,1}(X_{\rho,\mathcal{T}}) = \max_{\substack{\mathcal{T} \in \mathbb{R}^{n \times n} \\ \det(\mathcal{T}) \neq 0}} \min_{1 \leqslant i \leqslant m+n} \frac{StMa(\bar{\lambda}_{\rho,i})}{\sum_{j=1}^N |\partial \bar{\lambda}_{\rho,i}/\partial p_j|_{X = X_{\rho,\mathcal{T}}}}. \tag{25}$$

For the complex-valued matrix $M \in \mathbb{C}^{m \times n}$ with elements $M_{i,j}$, define a norm

$$\|M\|_s \triangleq \sum_{i=1}^{m} \sum_{j=1}^{n} |M_{i,j}|. \tag{26}$$

The optimization problem (25) is equivalent to the minimization problem

$$v_\rho = \frac{1}{\varphi_\rho} \triangleq \min_{\substack{\mathscr{T} \in \mathbb{R}^{n \times n} \\ \det(\mathscr{T}) \neq 0}} f_\rho(\mathscr{T}) \tag{27}$$

with the cost function

$$f_\rho(\mathscr{T}) = \max_{1 \leqslant i \leqslant m+n} \left\| \begin{bmatrix} I_l & 0 \\ 0 & \mathscr{T}^T \end{bmatrix} \Phi_{\rho,i} \begin{bmatrix} I_q & 0 \\ 0 & \mathscr{T}^{-T} \end{bmatrix} \right\|_s \tag{28}$$

and the constraint $\det(\mathscr{T}) \neq 0$, where

$$\Phi_{\rho,i} = \frac{\partial \bar{\lambda}_{\rho,i} / \partial X \big|_{X=X_{\rho,0}}}{StMa(\bar{\lambda}_{\rho,i})}. \tag{29}$$

In practice, the constrained optimization (27) is solved by solving for the unconstrained optimization problem

$$\tilde{v}_\rho \triangleq \min_{\mathscr{T} \in \mathbb{R}^{n \times n}} f_\rho(\mathscr{T}) \tag{30}$$

with a measure of monitoring the singular values of $\mathscr{T}$ to make sure that $\det(\mathscr{T}) \neq 0$. Because the cost function $f_\rho(\mathscr{T})$ is non-smooth and non-convex, non-gradient based optimization methods are required. The conventional non-gradient optimization algorithms, such as Rosenbrock and Simplex algorithms [1,6], in general can only obtain a local minimum solution. To overcome this difficulty, we adopt an efficient global optimization strategy based on the ASA. The ASA algorithm is detailed in [9,2].

4. Optimal FWL PID controller realizations

In this section, we specifically discuss the optimal realization problem of digital PID controllers. Let $(A_{\rho,C}^0 \in \mathbb{R}^{2 \times 2}, B_{\rho,C}^0 \in \mathbb{R}^{2 \times 1}, C_{\rho,C}^0 \in \mathbb{R}^{1 \times 2}, D_{\rho,C}^0 \in \mathbb{R})$ be an initial realization of the PID controller $C(\rho)$. From (27), the optimal PID controller realization problem is defined as

$$v_\rho \triangleq \min_{\substack{\mathscr{T} \in \mathbb{R}^{2 \times 2} \\ \det(\mathscr{T}) \neq 0}} \max_{1 \leqslant i \leqslant m+2} \left\| \begin{bmatrix} 1 & 0 \\ 0 & \mathscr{T}^T \end{bmatrix} \Phi_{\rho,i} \begin{bmatrix} 1 & 0 \\ 0 & \mathscr{T}^{-T} \end{bmatrix} \right\|_s. \tag{31}$$

The following theorem shows that the constrained optimization problem (31) can be decoupled into the two "simpler" unconstrained problems. First we define the two cost functions

$$f_{\rho,1}(x, y, w) = \max_{1 \leqslant i \leqslant m+2} \left\| \begin{bmatrix} w & 0 & 0 \\ 0 & x & 0 \\ 0 & y & 1/x \end{bmatrix} \Phi_{\rho,i} \begin{bmatrix} 1/w & 0 & 0 \\ 0 & 1/x & 0 \\ 0 & -y & x \end{bmatrix} \right\|_s \tag{32}$$

and

$$f_{\rho,2}(x, y, u, w) = \max_{1 \leqslant i \leqslant m+2} \left\| \begin{bmatrix} w & 0 & 0 \\ 0 & x & u \\ 0 & (xy-1)/u & y \end{bmatrix} \Phi_{\rho,i} \begin{bmatrix} 1/w & 0 & 0 \\ 0 & y & -u \\ 0 & (1-xy)/u & x \end{bmatrix} \right\|_s. \tag{33}$$

Theorem 2. *Let*

$$v_{\rho,1} = \min_{\substack{x \in (0,+\infty) \\ y \in (-\infty,+\infty) \\ w \in (0,+\infty)}} f_{\rho,1}(x, y, w) \tag{34}$$

and

$$v_{\rho,2} = \min_{\substack{x\in(-\infty,+\infty) \\ y\in(-\infty,+\infty) \\ u\in(0,+\infty) \\ w\in(0,+\infty)}} f_{\rho,2}(x,y,u,w). \tag{35}$$

Then

$$v_\rho = \min\{v_{\rho,1}, v_{\rho,2}\}. \tag{36}$$

If $v_\rho = v_{\rho,1}$ and $(x_{opt1}, y_{opt1}, w_{opt1})$ is the optimal solution of the problem (34), the optimal solution of the problem (31) is given as

$$\mathscr{T}_{opt} = \frac{1}{w_{opt1}} \begin{bmatrix} x_{opt1} & y_{opt1} \\ 0 & 1/x_{opt1} \end{bmatrix}. \tag{37}$$

If $v_\rho = v_{\rho,2}$ and $(x_{opt2}, y_{opt2}, u_{opt2}, w_{opt2})$ is the optimal solution of the problem (35), the optimal solution of the problem (31) is given as

$$\mathscr{T}_{opt} = \frac{1}{w_{opt2}} \begin{bmatrix} x_{opt2} & (x_{opt2}\,y_{opt2} - 1)/u_{opt2} \\ u_{opt2} & y_{opt2} \end{bmatrix}. \tag{38}$$

The proof of Theorem 2 is identical to the special case of $\rho = z$ given in [3]. Because $f_{\rho,1}(x,y,w)$ and $f_{\rho,2}(x,y,u,w)$ are both non-smooth and non-convex functions, we will adopt the ASA algorithm to solve for the two corresponding optimization problems.

5. Numerical examples

Example 1. This was the IFAC93 benchmark PID control system [13]. The continuous-time plant model was

$$P(s) = \frac{25(-0.4s + 1)}{(s^2 + 3s + 25)(5s + 1)} \tag{39}$$

and the designed PID controller was

$$C(s) = 1.311 + \frac{0.431}{s} + \frac{1.048s}{1 + 12.92s}. \tag{40}$$

The sampled-data system with the infinite-precision controller was stable when the sampling period $h \leqslant 2^3$. The range of the sampling period used in the simulation was 2^3 to 2^{-12}. Given a sampling rate, the discrete-time plant model $P(\rho)$ and the digital controller $C(\rho)$ were obtained using the MATLAB discretizing routines, which are based on the bilinear (Tustin) transformation. The initial controller realization $X_{\rho,0}$ was chosen to be the "controllable canonical" form. When $X_{\rho,0}$ was provided, the eigenvalues $\{\bar{\lambda}_{\rho,i}\}$ of the ideal closed-loop system and the eigenvalue sensitivity matrices $\{\Phi_{\rho,i}\}$ were computed. The ASA algorithm was then used to search for an optimal transformation matrix $\mathscr{T}_{opt}$ by solving for the optimization problem (31) using Theorem 2. This produced the corresponding optimal controller realization $X_{\rho,opt}$ that maximizes $\mu_{\rho,1}(X_{\rho,\mathscr{T}})$.

Fig. 2 compares the values of $\mu_{\rho,1}$ given different sampling rates, for the initial and optimal controller realizations. It can be seen from Fig. 2(a) and (b) that optimization effectively improved the FWL closed-loop stability performance for both the z and δ based controller structures. Fig. 2(c) shows that the optimal δ-based controller realization has a much larger FWL closed-loop stability margin than its z-based counterpart. Furthermore, as the sampling rate increases, the stability measure for $X_{\delta,opt}$ improves slightly and eventually levels out while the stability measure for $X_{z,opt}$ decreases exponentially. This confirms with a well-known fact that the δ parameterization has some important advantages over the z parameterization, especially under fast sampling conditions. Fig. 3 depicts the estimated minimum bit lengths for the optimal z and δ realizations. As mentioned previously, for the δ parameterization, the sampling period h should be implemented exactly without FWL errors. Even taking into account this requirement, the optimal δ-based realization requires a smaller bit length in FWL implementation.
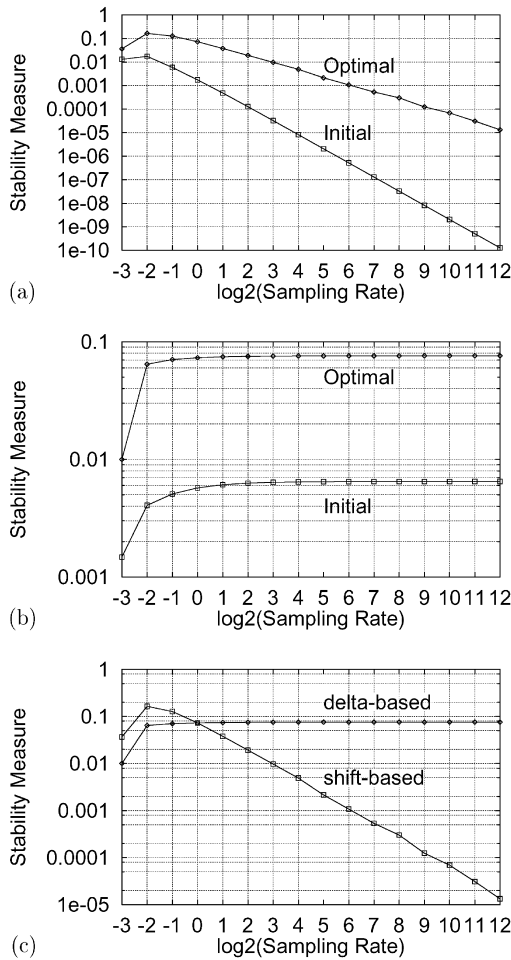
Fig. 2. FWL stability measure $\mu_{\rho,1}$ as a function of sampling rate for different controller realizations. IFAC93 benchmark PID controller system. (a) Initial and optimal z-based controller realizations. (b) Initial and optimal δ-based controller realizations. (c) Optimal z and δ-based controller realizations.

Example 2. This was the control system cited in [5]. The continuous-time plant model was given by

$$P(s) = \frac{1.6188s^2 - 0.1575s - 43.9425}{(s^4 + 0.1736s^3 + 27.9001s^2 + 0.0186s)(s + 1)} \tag{41}$$

and the continuous-time stabilizing controller designed using the $\mathscr{H}_\infty$ method was

$$C(s) = \frac{0.046s^6 + 1.5862s^5 + 3.09s^4 + 44.3s^3 + 42.7785s^2 + 0.02867s + 1.58 \times 10^{-4}}{s^6 + 3.766s^5 + 34.9509s^4 + 106.2s^3 + 179.2s^2 + 166.43s + 0.0033}. \tag{42}$$

The range of the sampling rate tested in the simulation was 2^1 to 2^{12}. Again, the discretizing routines in MATLAB were used to obtain the discrete-time plant model $P(\rho)$ and the digital controller $C(\rho)$. The ASA algorithm was used to solve for the resulting optimization problem (27). This produced the corresponding optimal realization $X_{\rho,\text{opt}}$. For this example, the controller order was $n = 6$ and the optimization space had a dimension of $n \times n = 36$. This was by no means a small task, and the ASA algorithm performed very efficiently.

Fig. 4 compares the values of the FWL stability measure for $X_{z,\text{opt}}$ and $X_{\delta,\text{opt}}$, respectively, under various sampling conditions. Fig. 5 shows the corresponding estimated minimum bit length that can guarantee the
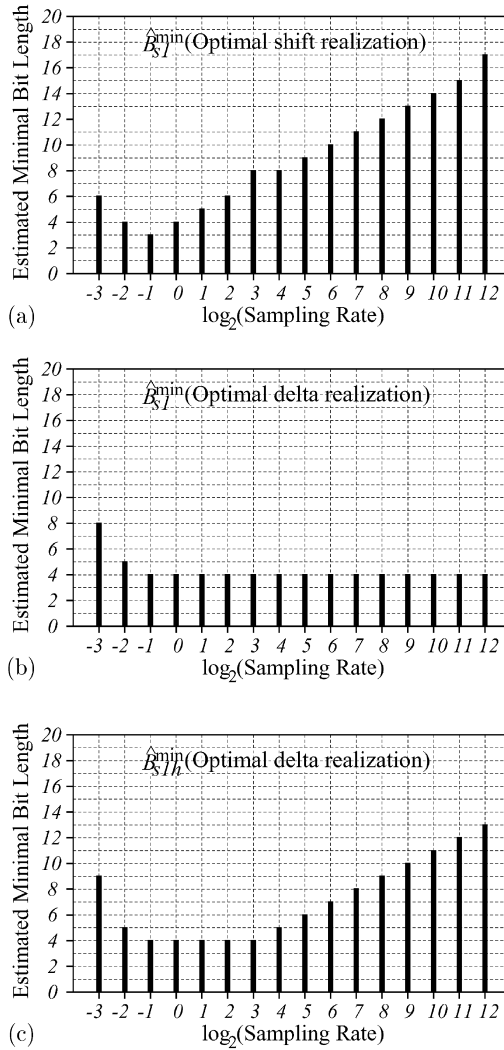
Fig. 3. Estimated minimum bit length as a function of sampling rate for different controller realizations. IFAC93 benchmark PID controller system. (a) $\hat{B}_s^{\min}$ based on $\mu_{z,1}$ for the optimal z-based realization $X_{z,\text{opt}}$. (b) $\hat{B}_s^{\min}$ based on $\mu_{\delta,1}$ only for the optimal δ-based realization $X_{\delta,\text{opt}}$. (c) $\hat{B}_{sh}^{\min}$ based on $\mu_{\delta,1}$ and h for the optimal δ-based realization $X_{\delta,\text{opt}}$.

closed-loop stability for these two controller realizations. In this example, $\hat{B}_s^{\min} = \hat{B}_{sh}^{\min}$ for the δ realization $X_{\delta,\text{opt}}$. The results clearly show the advantages of the δ parameterization over the z operator approach.

6. Conclusions

The paper addresses the issues of closed-loop stability of sampled-data control systems with FWL implemented digital controllers. A unified stability measure quantifying the robustness of closed-loop stability to the FWL effects in both the z and δ domains has been presented. It has been shown that the optimal FWL controller realization can be interpreted as a constrained nonlinear optimization problem. In particular, for PID controller realizations, the optimization can be decoupled into two unconstrained optimization problems. A comparative study using two numerical examples have been given to optimize closed-loop stability bounds
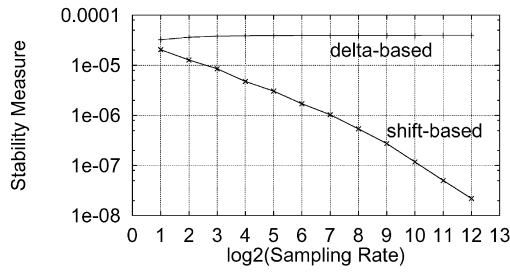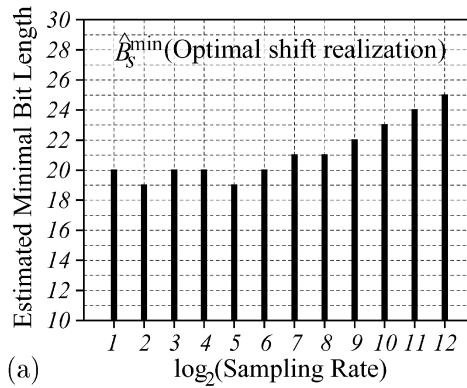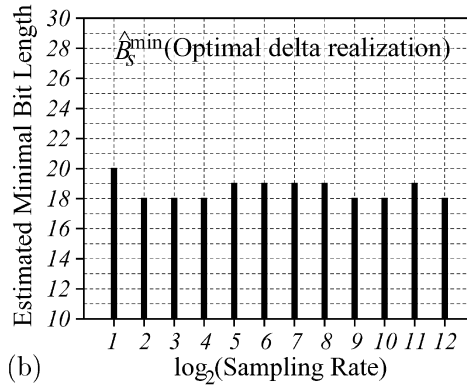
Fig. 4. FWL stability measure $\mu_{\rho,1}$ as a function of sampling rate for the two different optimal controller realizations. The high-order controller system.

Fig. 5. Estimated minimum bit length as a function of sampling rate for the two different optimal controller realizations. The high-order controller system. (a) $\hat{B}_s^{min}$ for the optimal z-based realization $X_{z,opt}$. (b) $\hat{B}_s^{min}(=\hat{B}_{sh}^{min})$ for the optimal δ-based realization $X_{\delta,opt}$.

of finite-precision controller structures for both the z and δ operator parameterizations. The results obtained demonstrate that the digital controllers described with the δ operator has better closed-loop stability robustness to FWL effects, particularly in fast sampling conditions.

Acknowledgements

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